

FIG. 1A

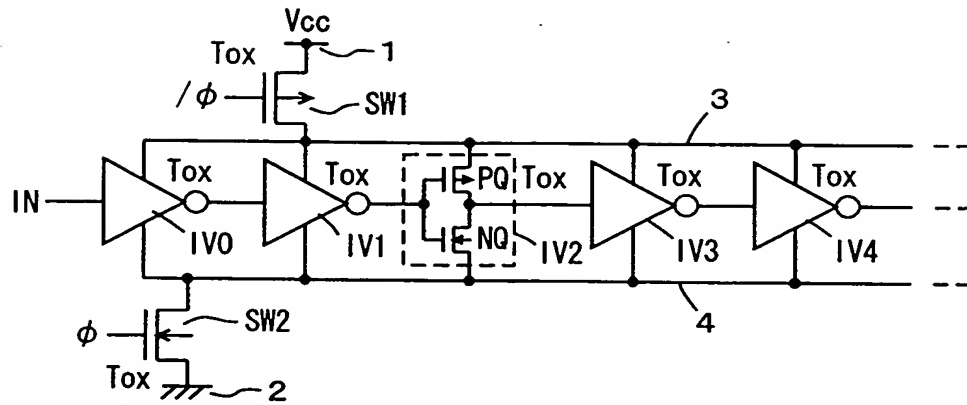


FIG. 1B

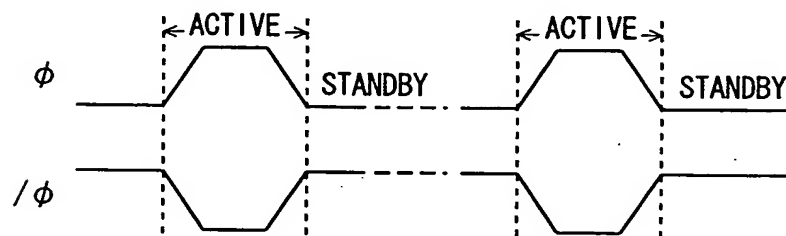


FIG. 2A

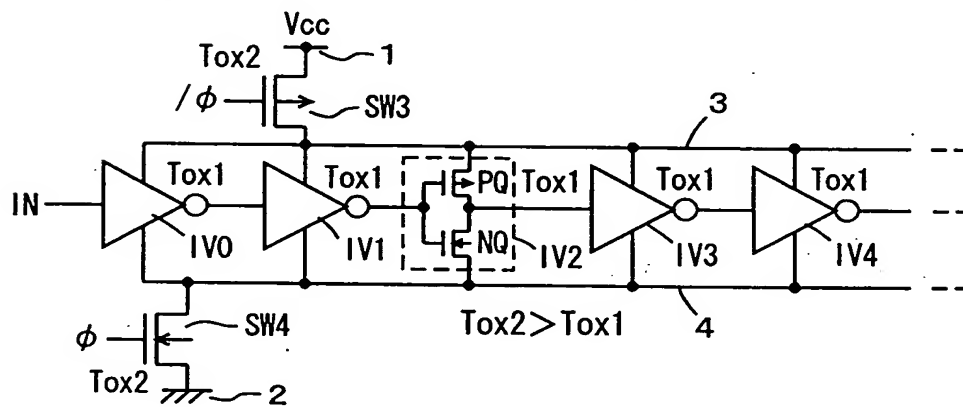


FIG. 2B

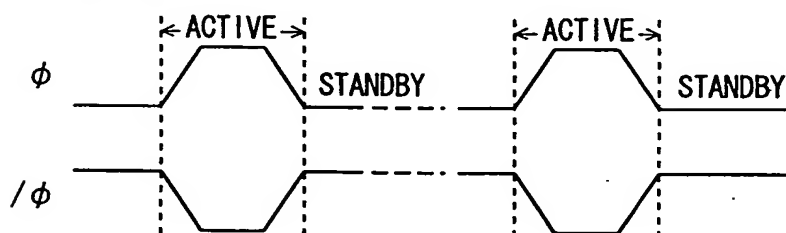


FIG. 3A

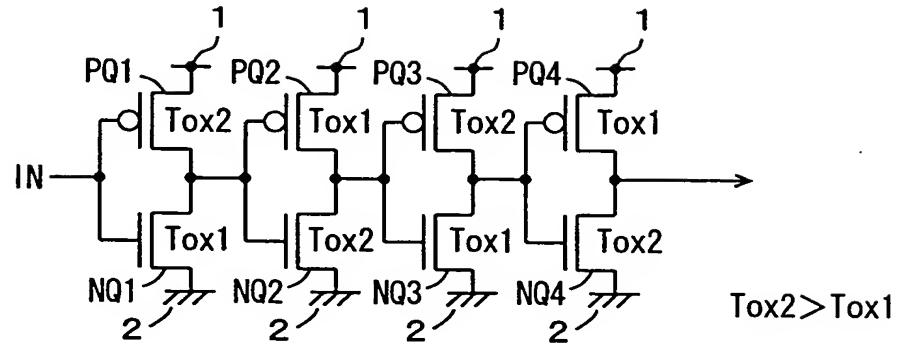


FIG. 3B

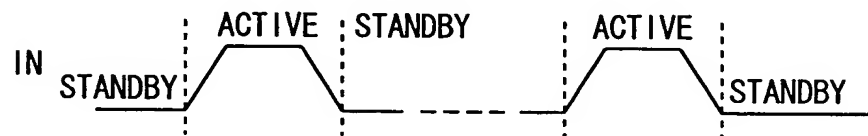


FIG. 4

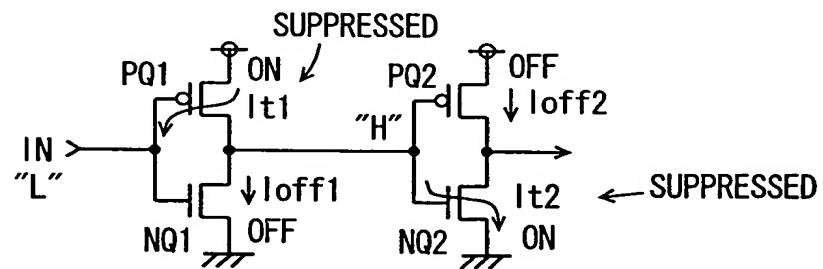


FIG. 5

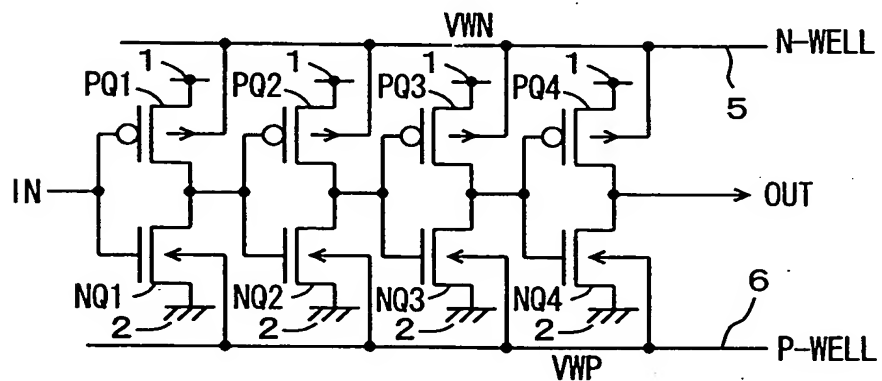


FIG. 6

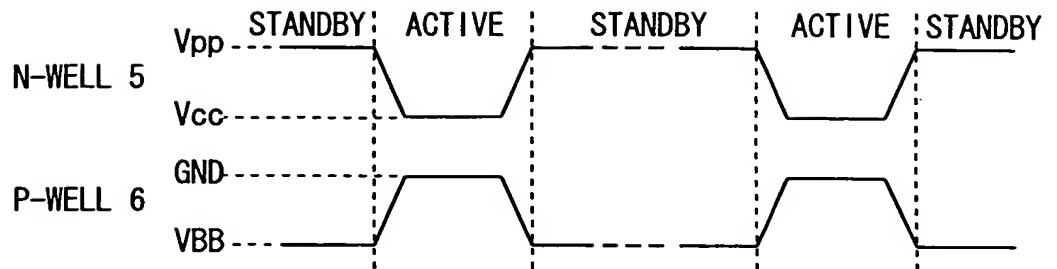


FIG. 7

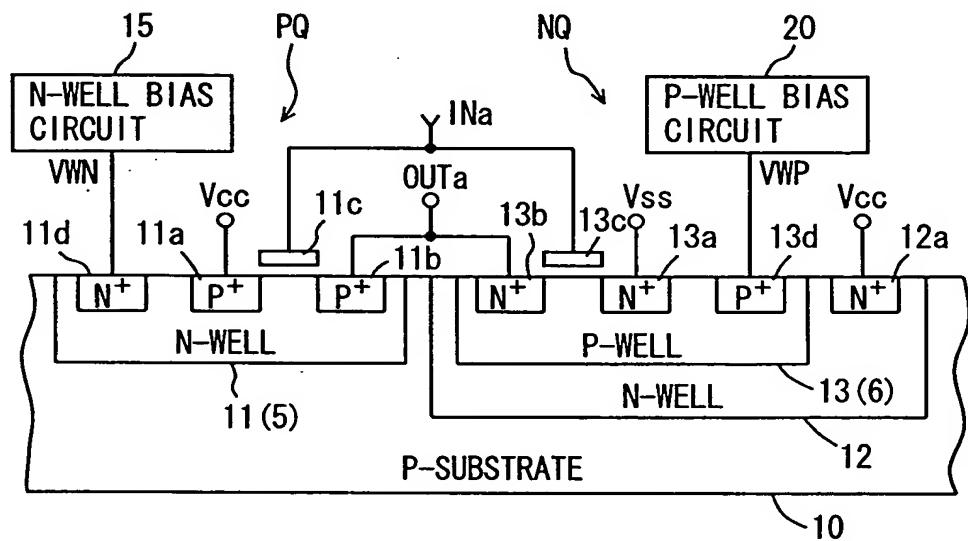


FIG. 8A

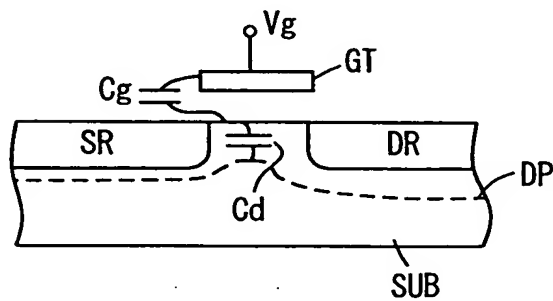


FIG. 8B

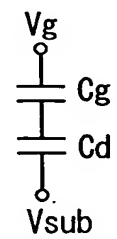


FIG. 9

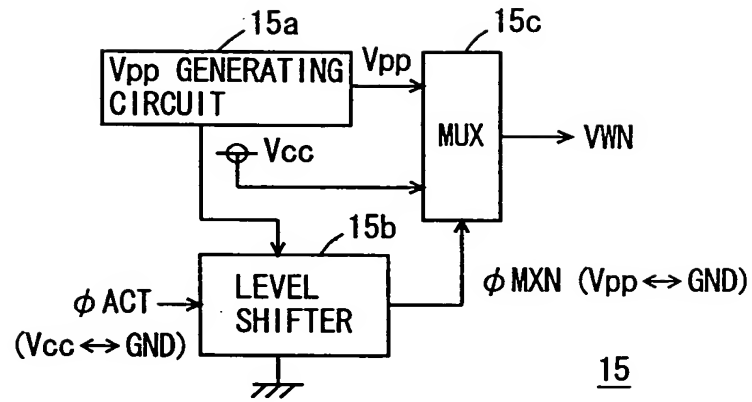


FIG. 10

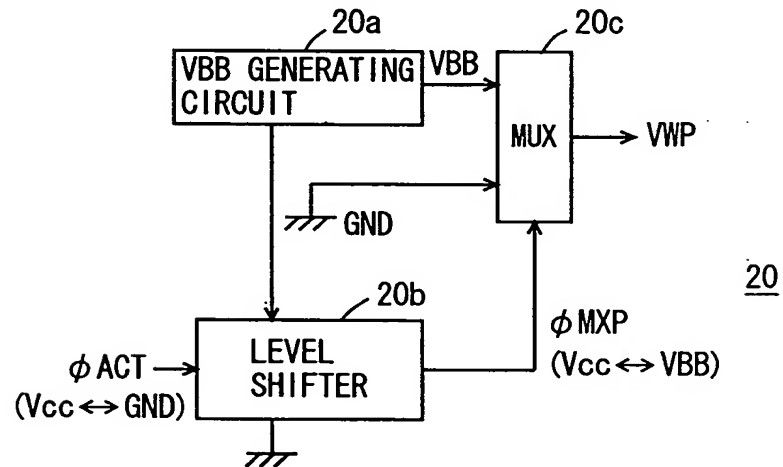


FIG. 11

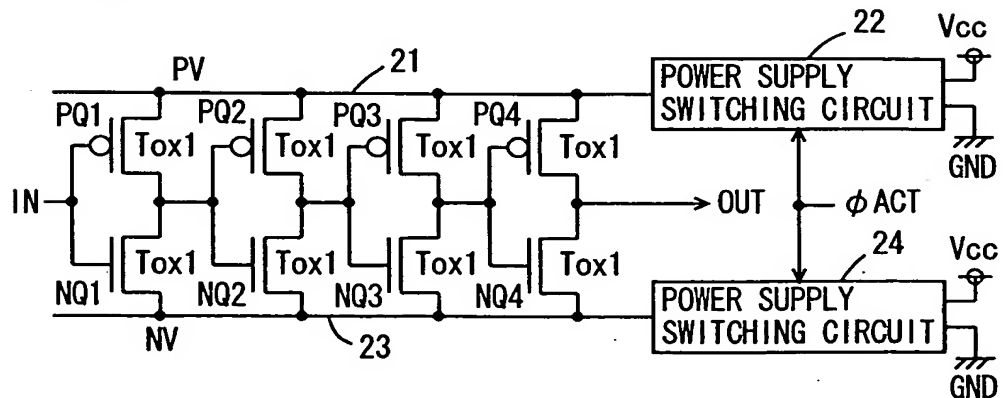


FIG. 12

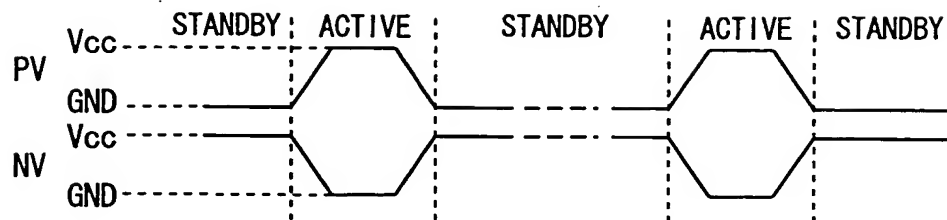


FIG. 13

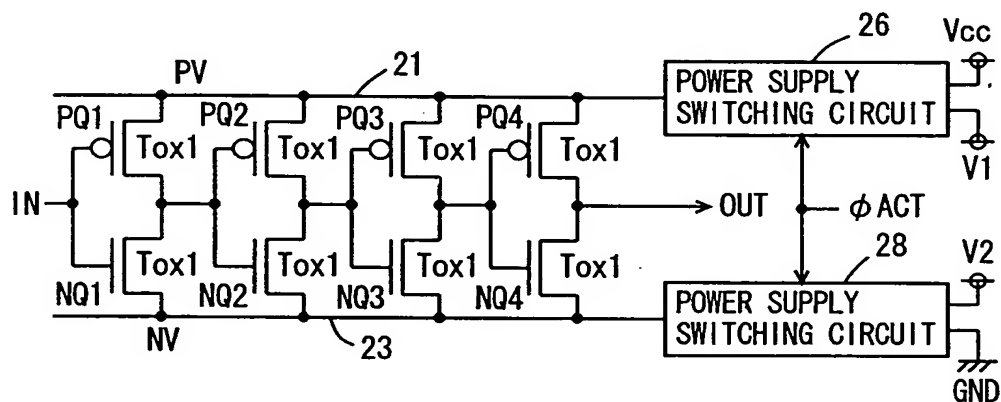


FIG. 14

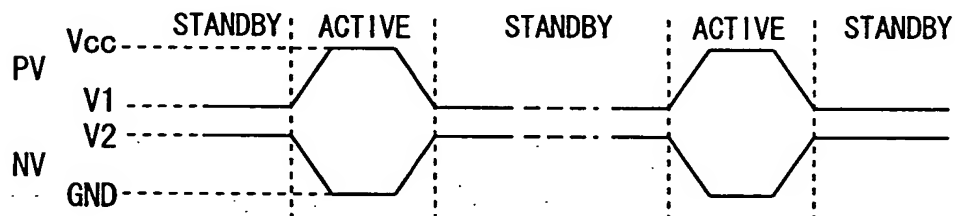


FIG. 15

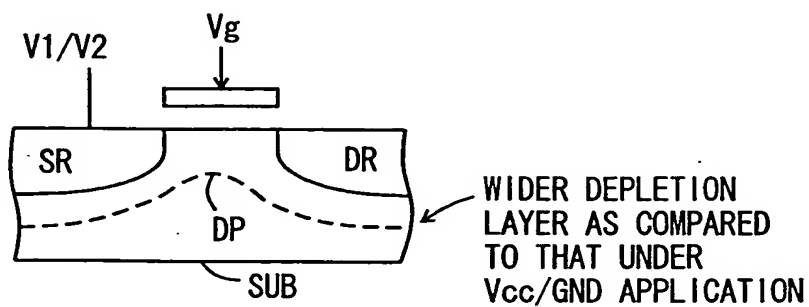


FIG. 16

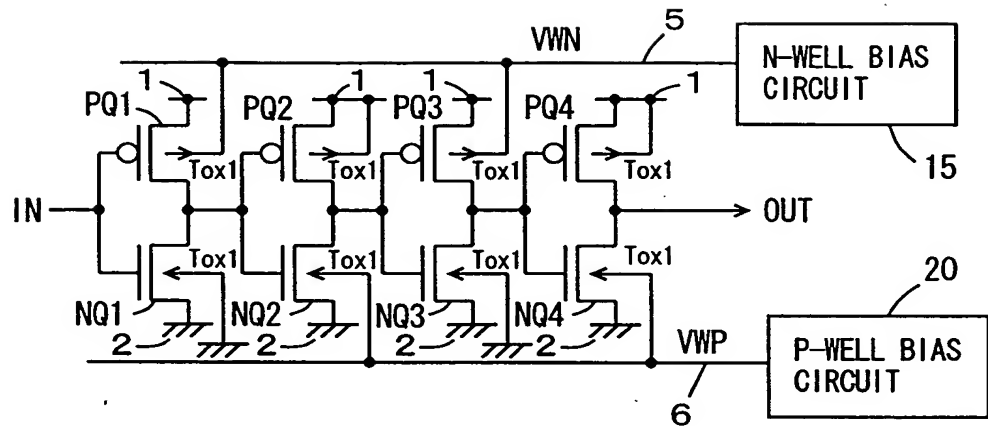


FIG. 17

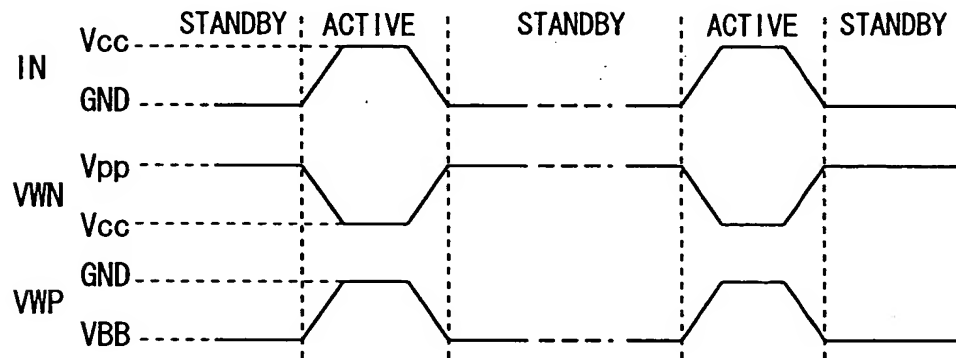


FIG. 18

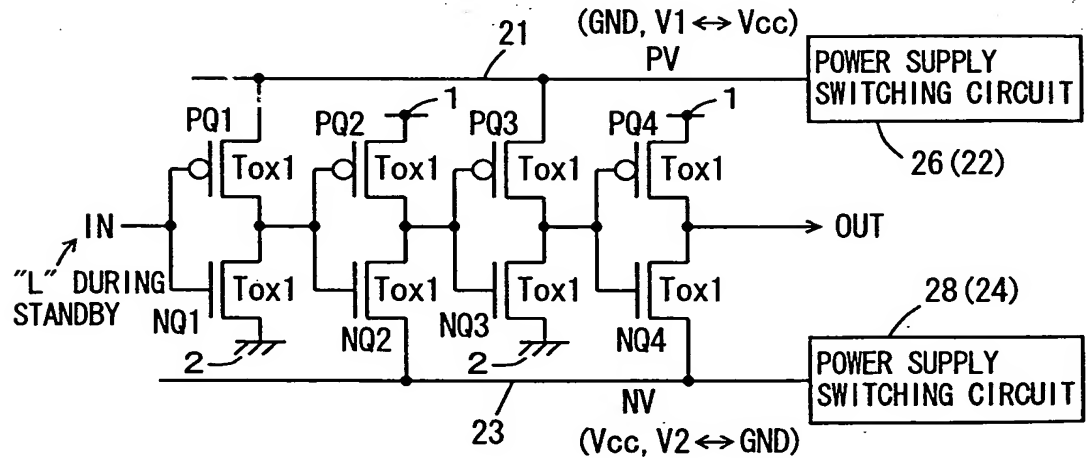


FIG. 21 A

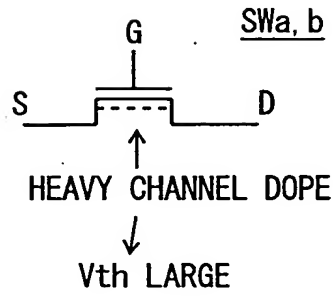


FIG. 21 B

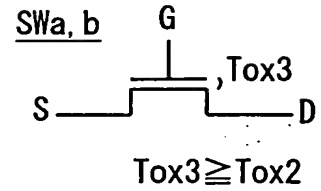


FIG. 21 C

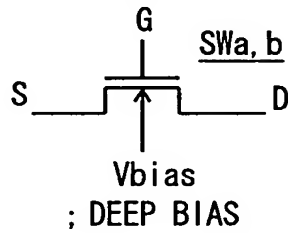


FIG. 22

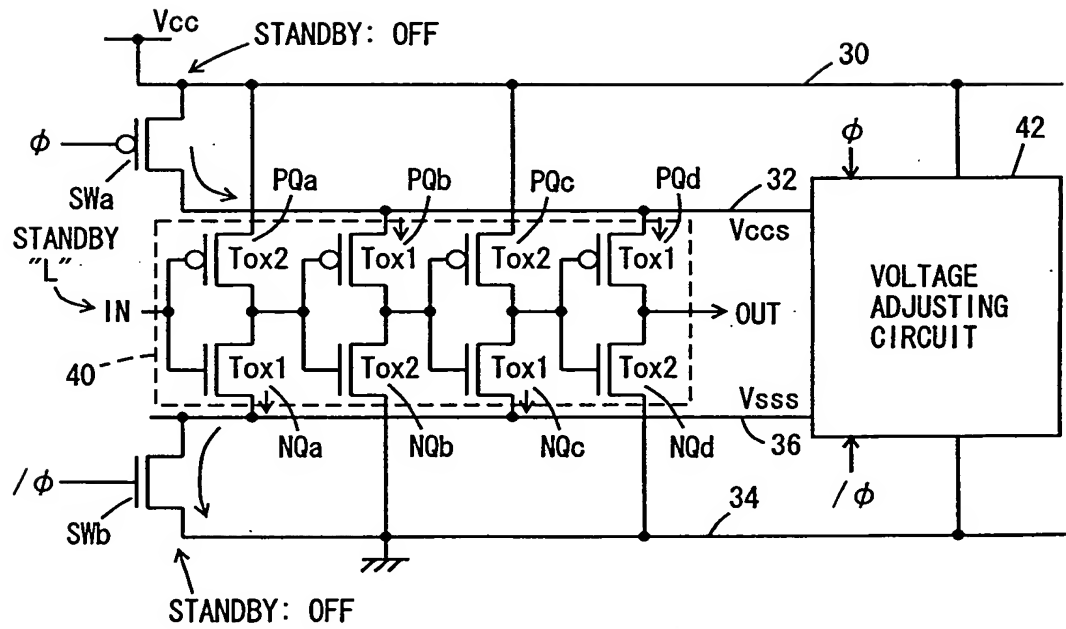


FIG. 23

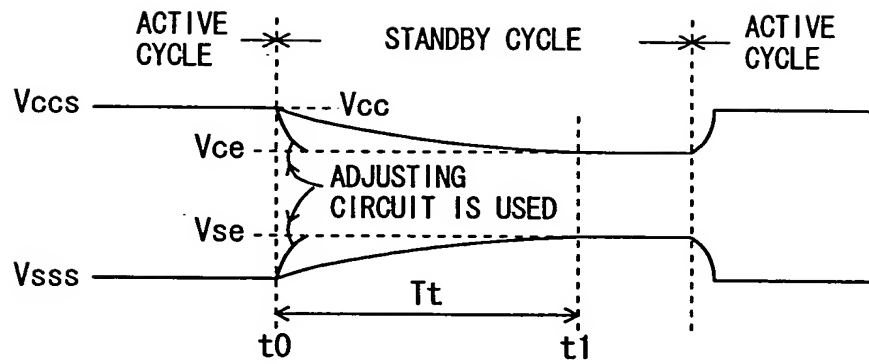


FIG. 24

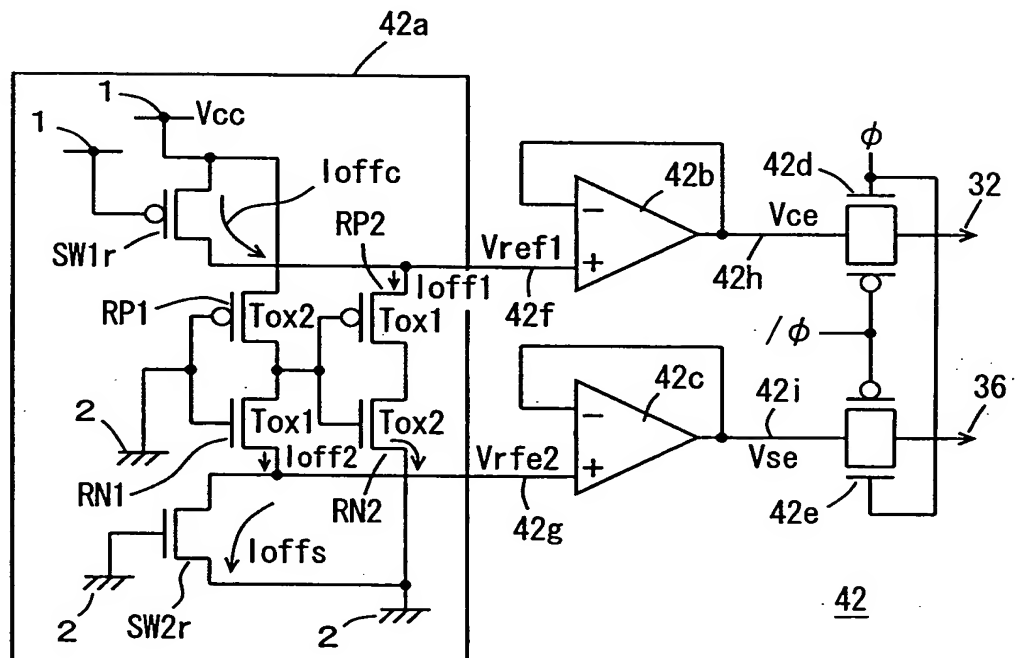


FIG. 25A

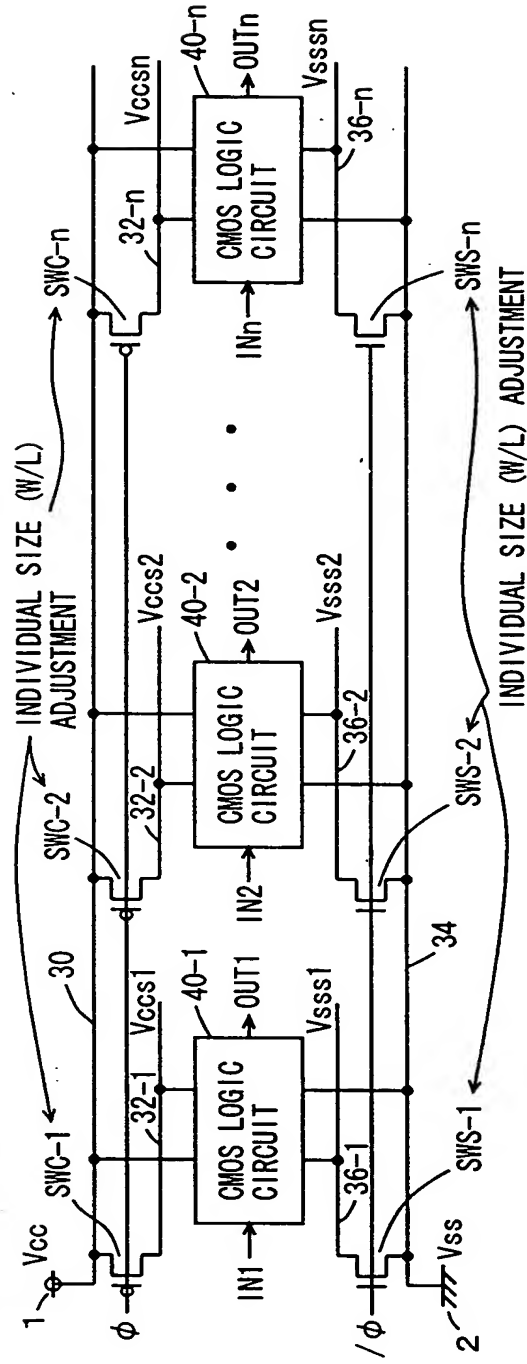


FIG. 25B

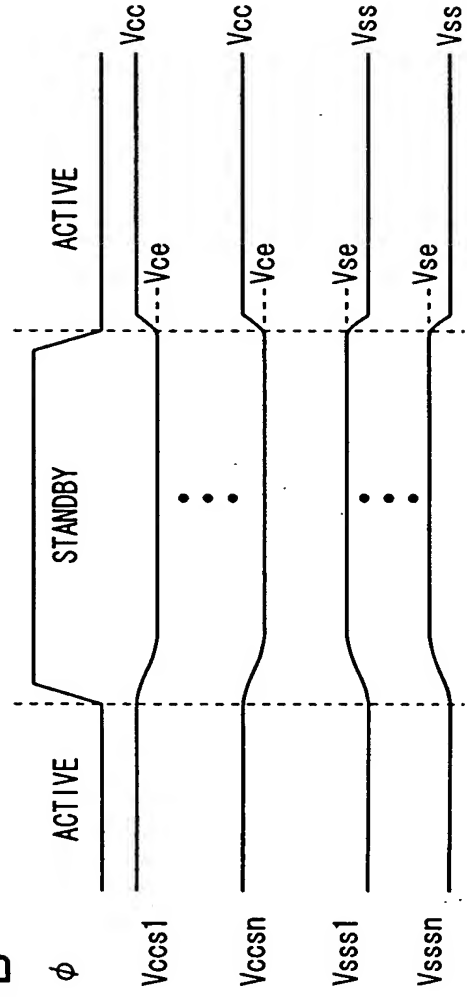


FIG. 27

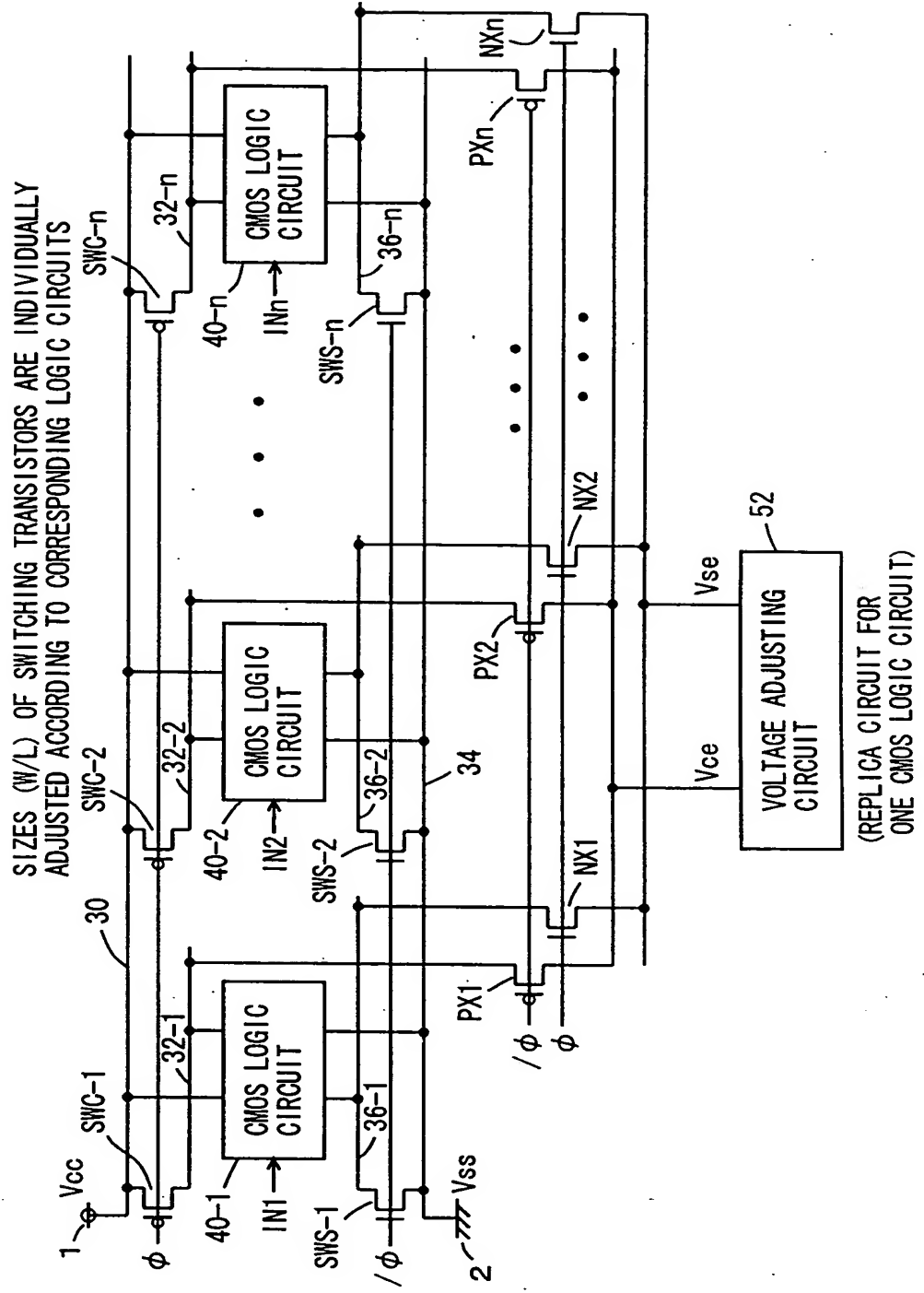


FIG. 28

:SIZES (W/L) OF SWITCHING TRANSISTORS ARE INDIVIDUALLY
ADJUSTED ACCORDING TO CORRESPONDING LOGIC CIRCUITS:

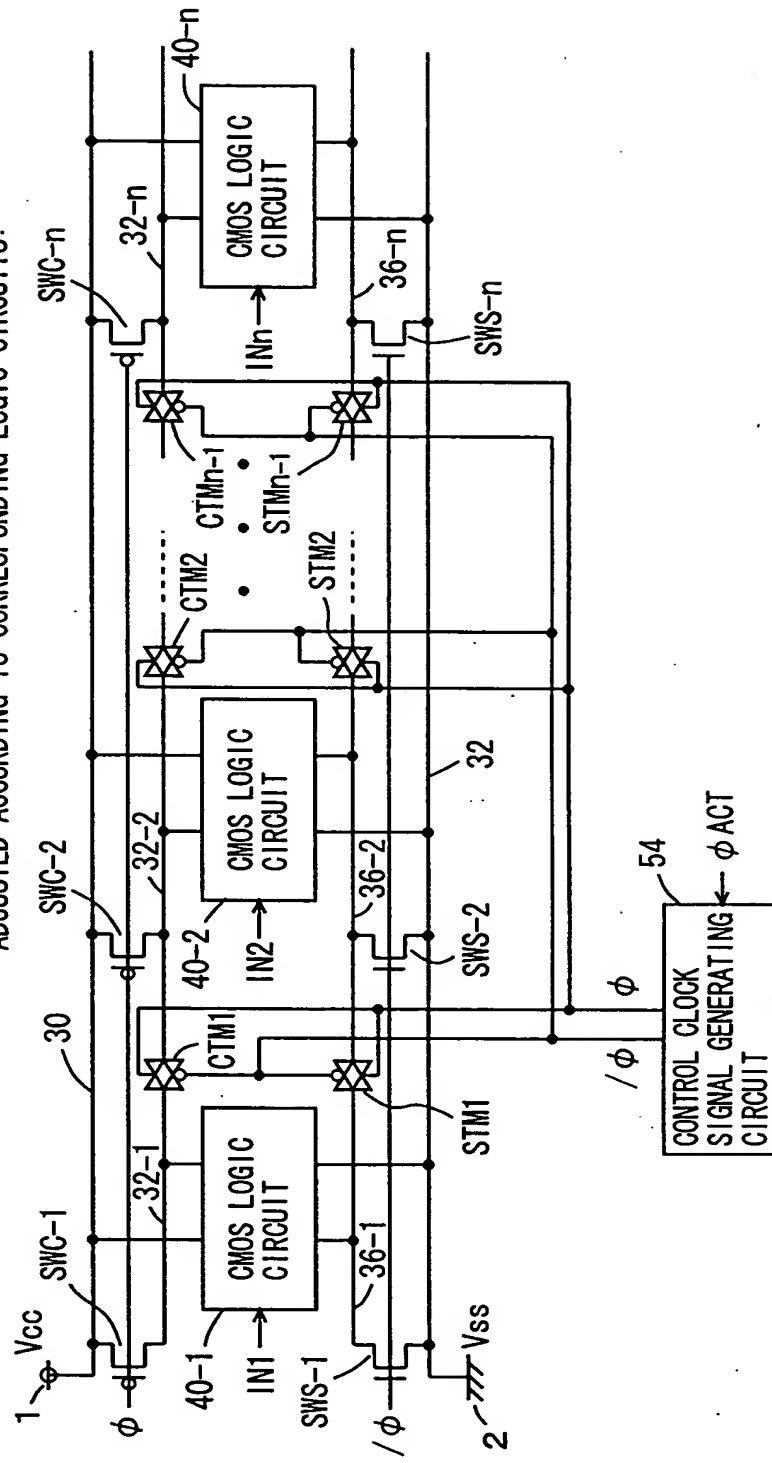


FIG. 29

:SIZES (W/L) OF SWITCHING TRANSISTORS ARE INDIVIDUALLY ADJUSTED ACCORDING TO CORRESPONDING LOGIC CIRCUITS:

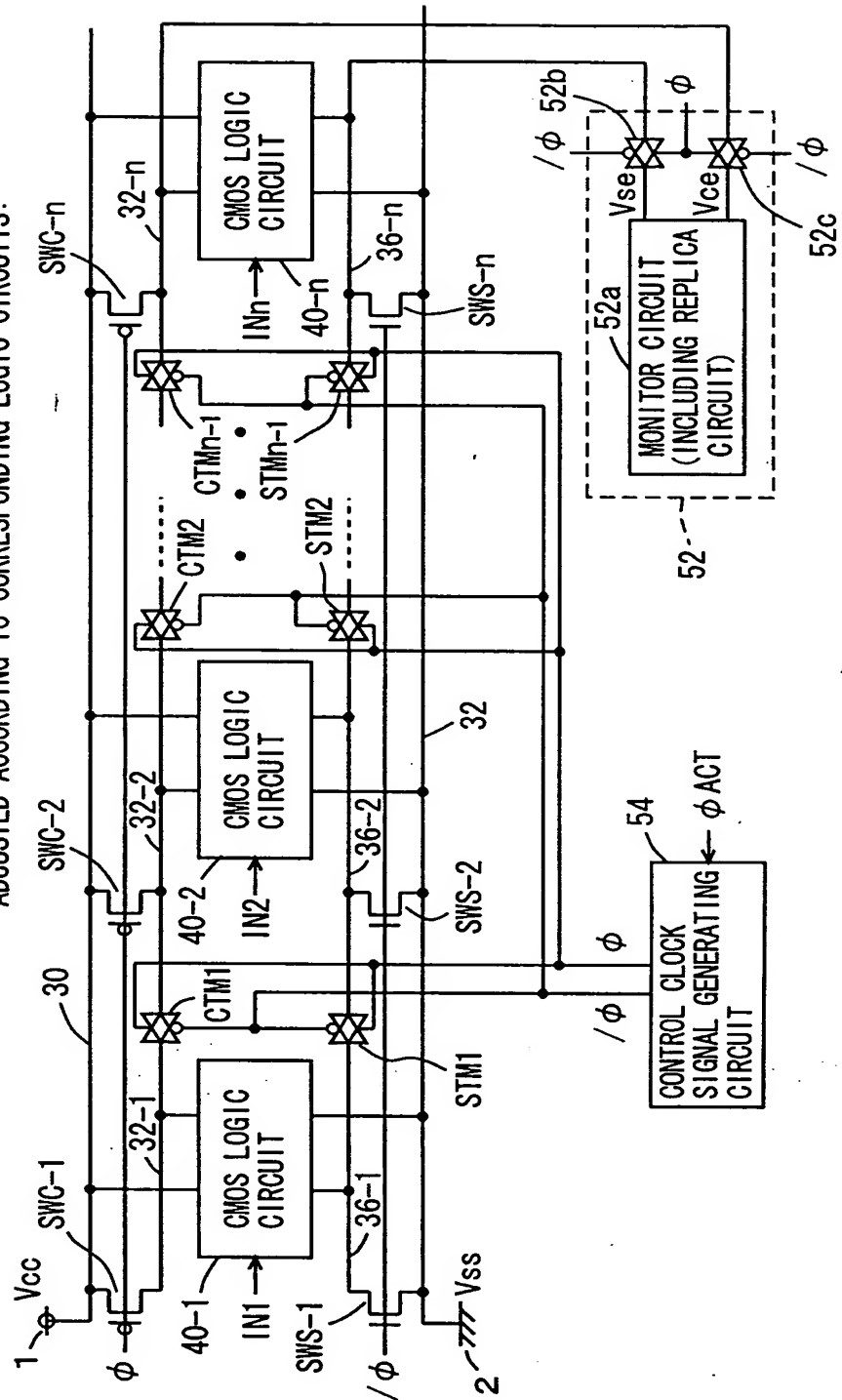


FIG. 30

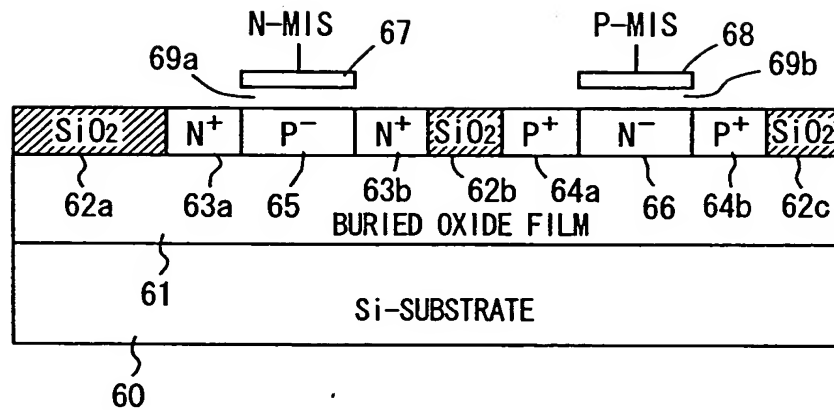


FIG. 31A

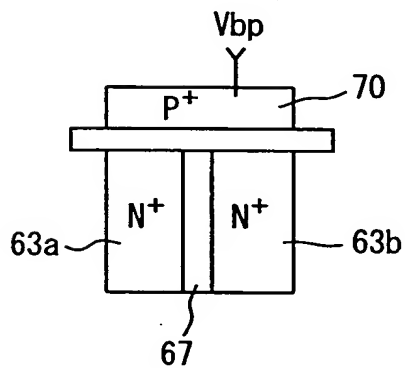


FIG. 31B

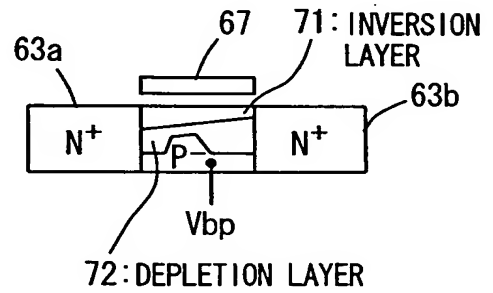


FIG. 32

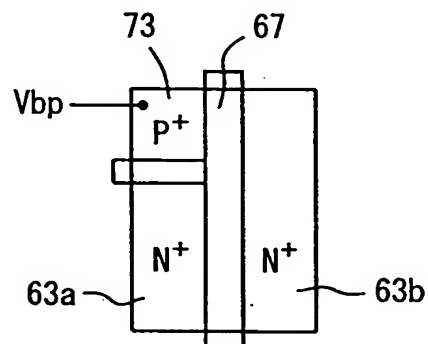


FIG. 33 A

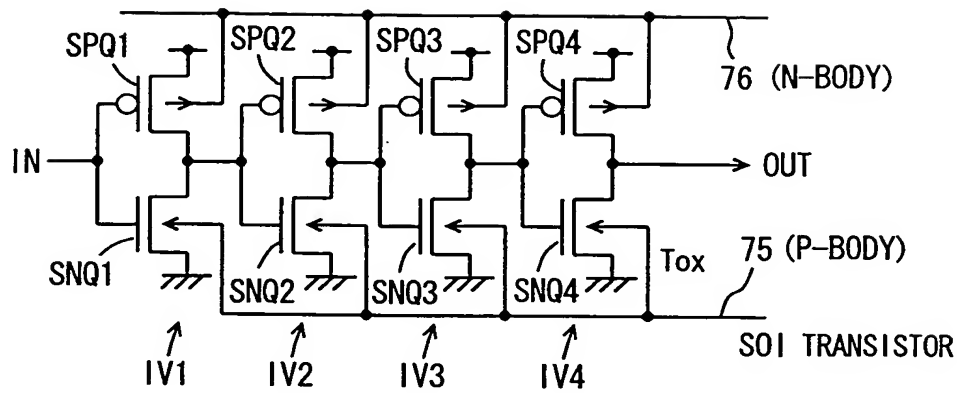


FIG. 33 B

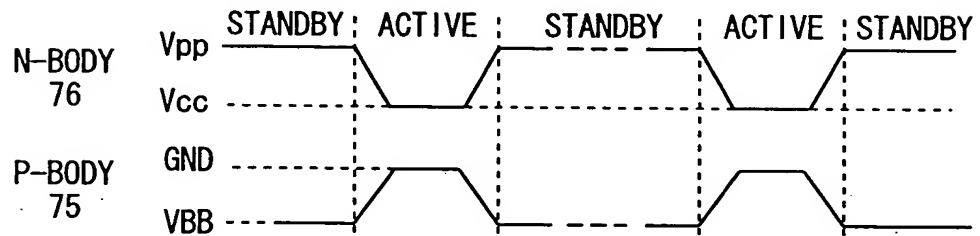


FIG. 34 A

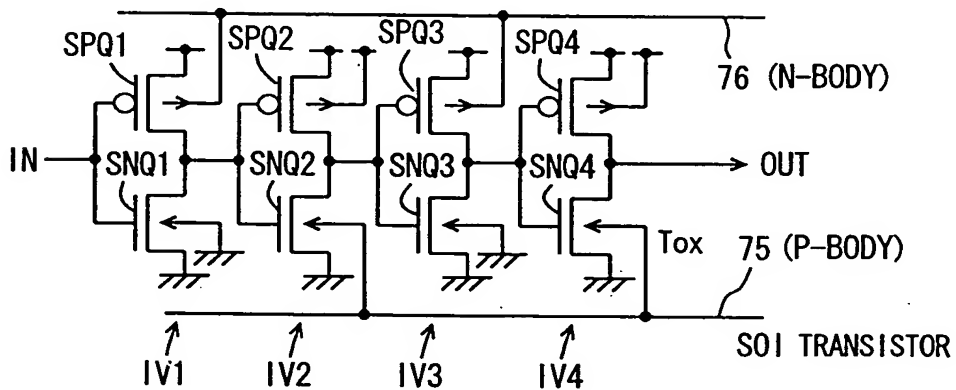


FIG. 34 B

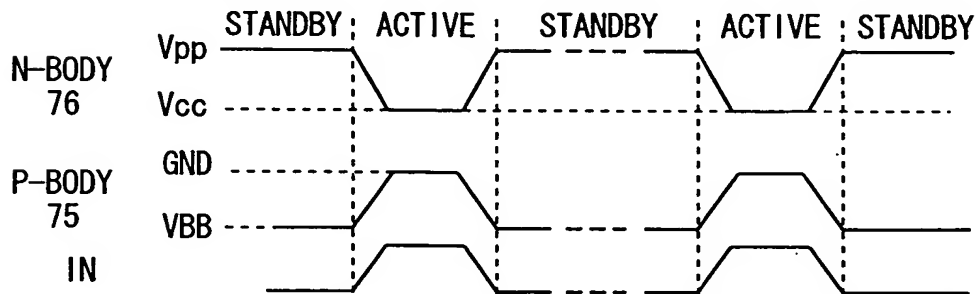


FIG. 35

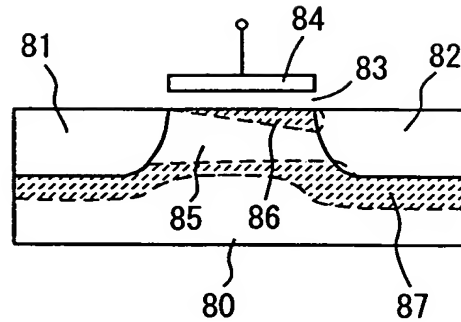


FIG. 36A

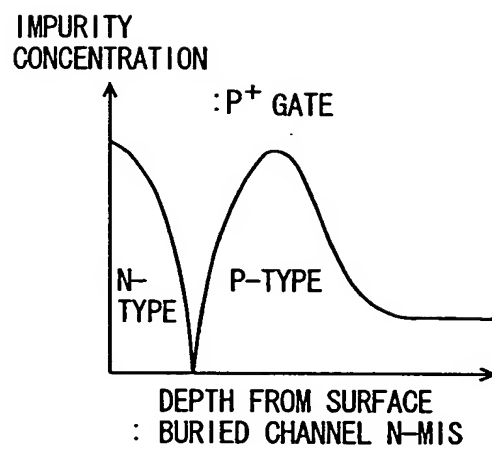


FIG. 36B

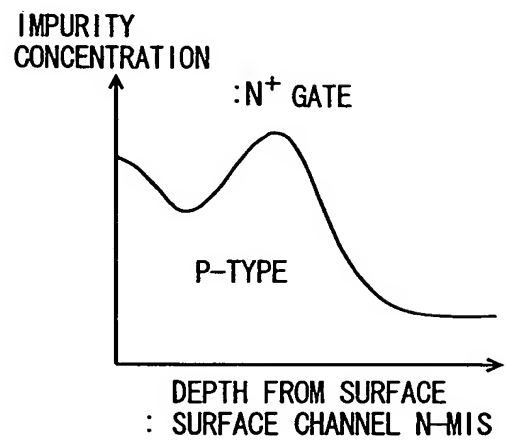


FIG. 37A

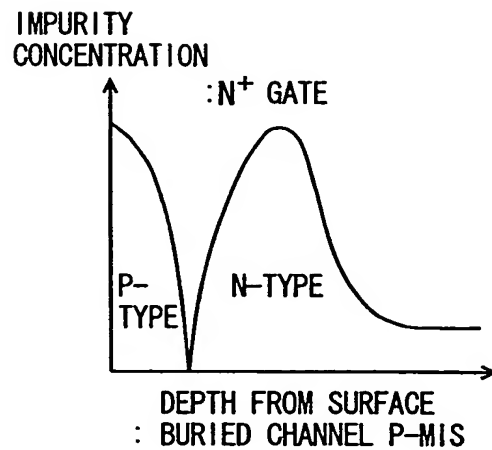


FIG. 37B

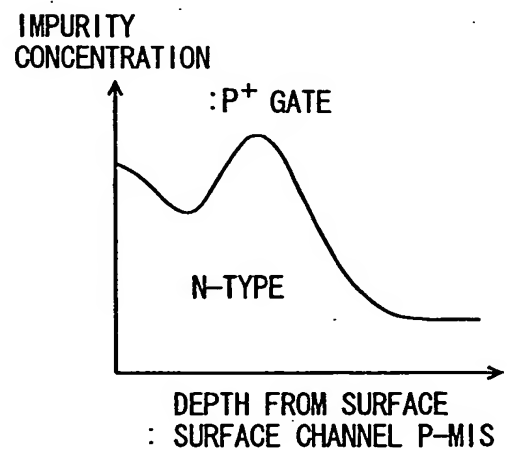


FIG. 38A

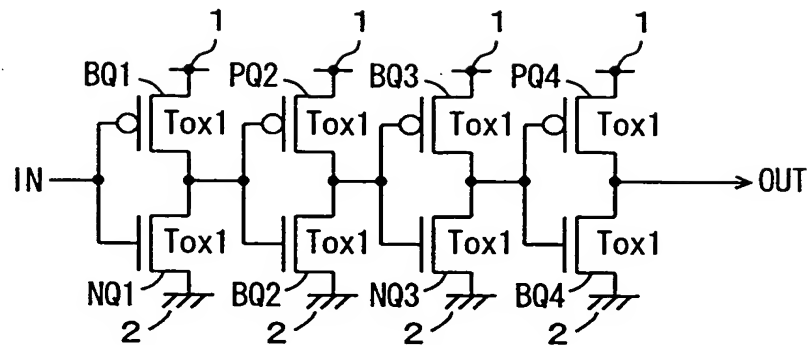


FIG. 38B

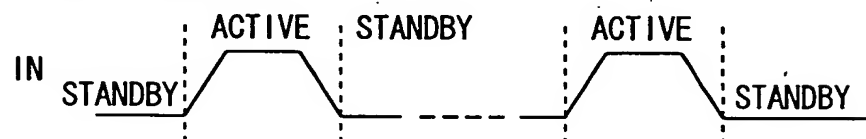


FIG. 39A

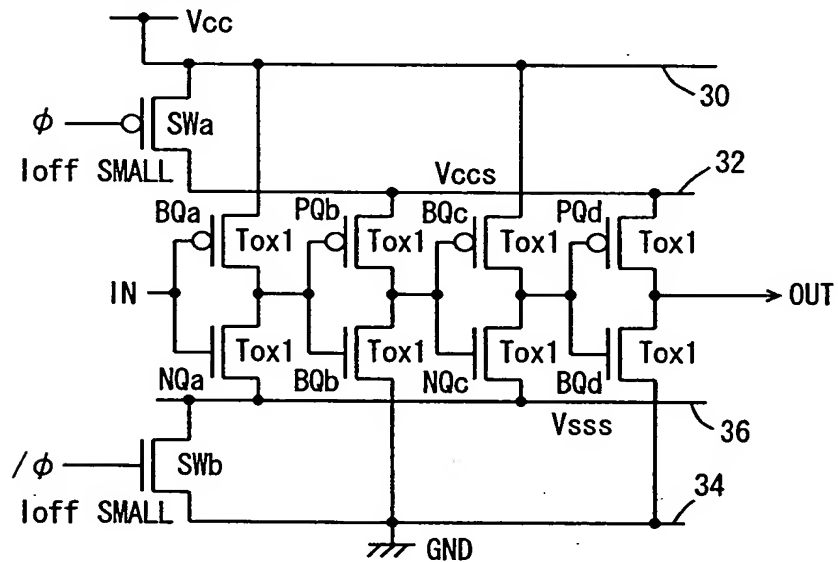


FIG. 39B

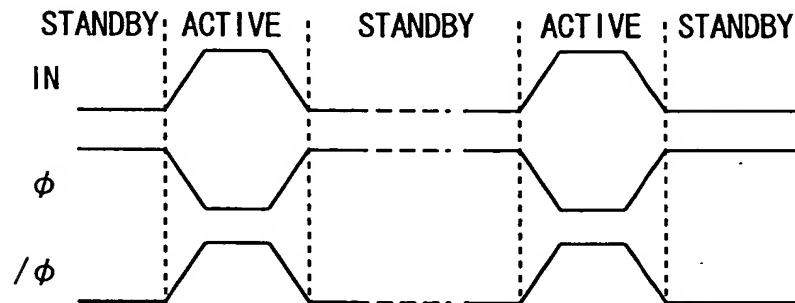


FIG. 40A

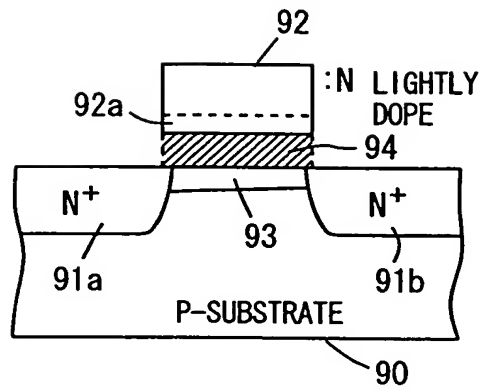


FIG. 40B

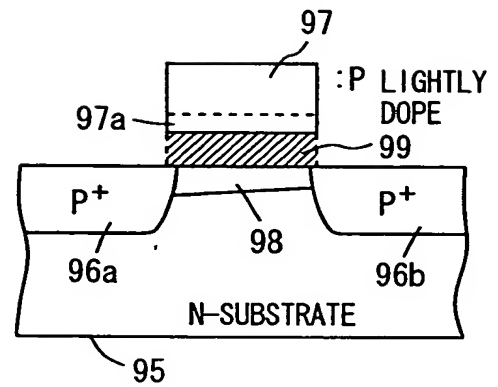


FIG. 41

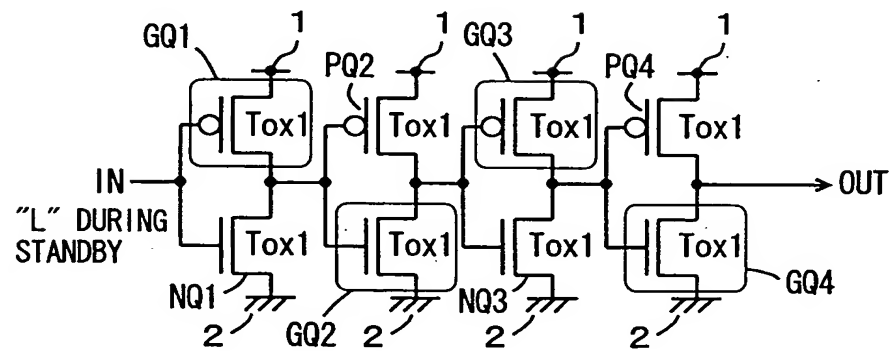


FIG. 42

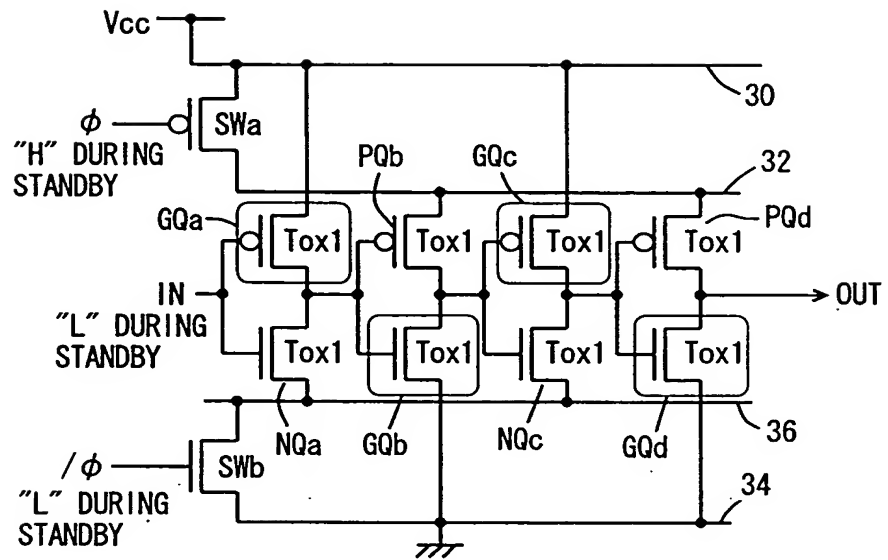


FIG. 43

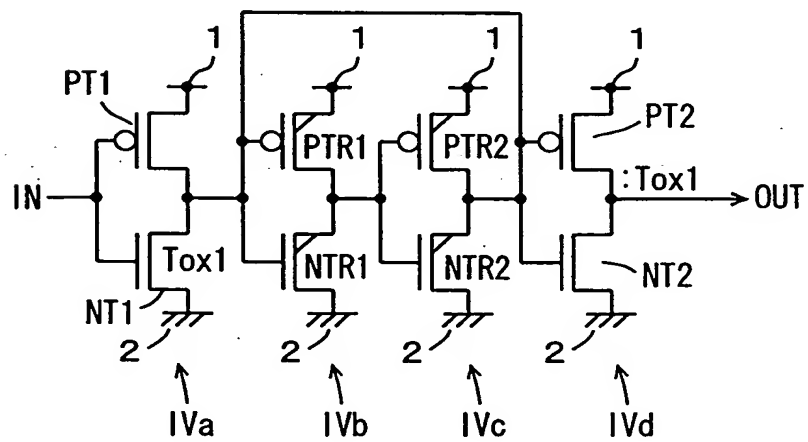


FIG. 44

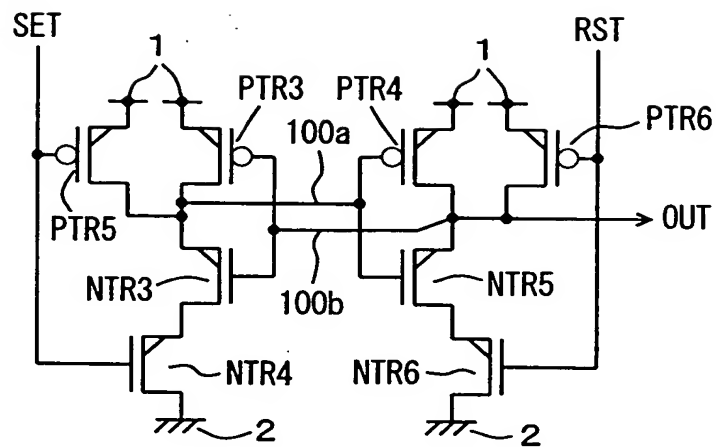


FIG. 45

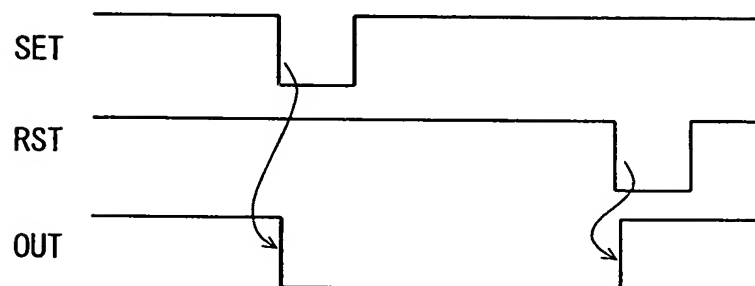


FIG. 46

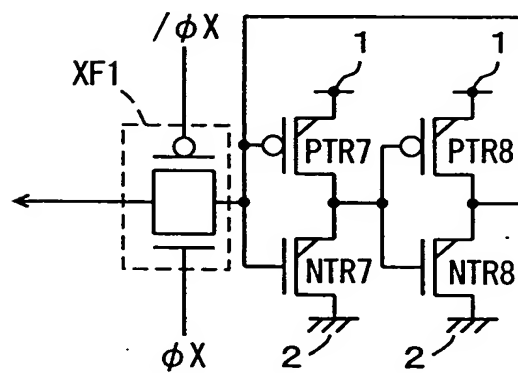


FIG. 47

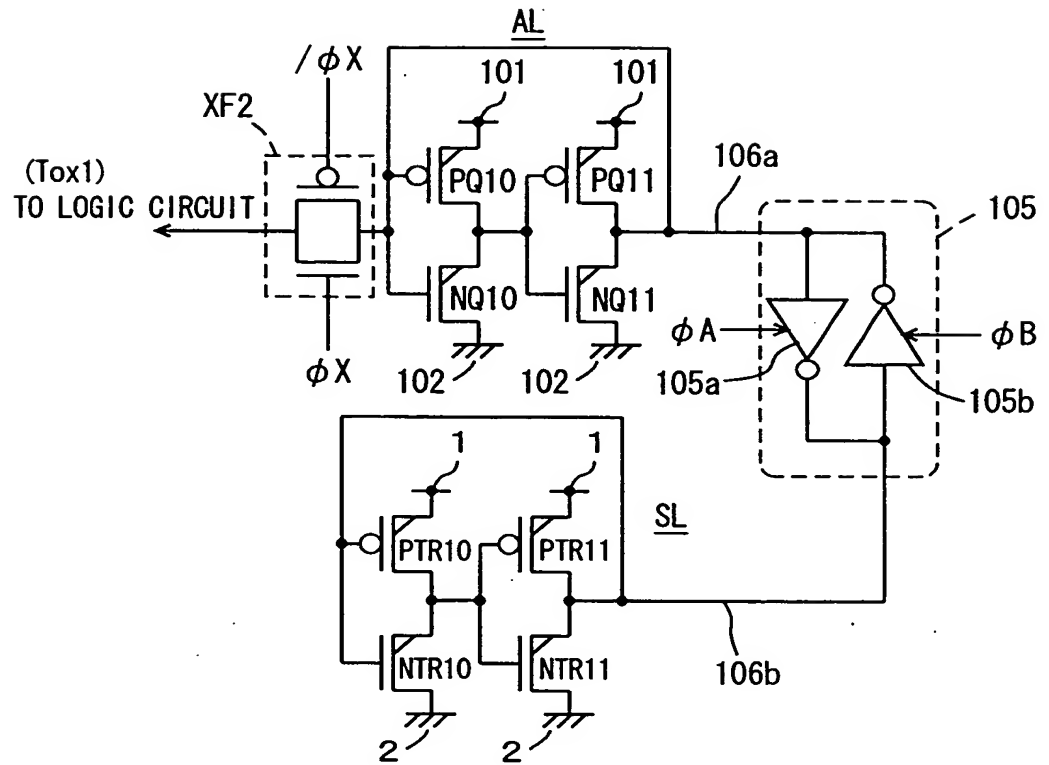


FIG. 48

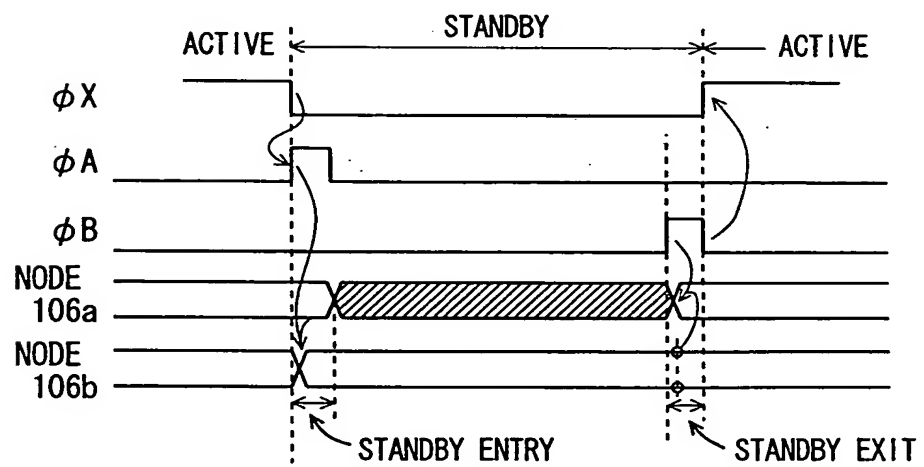


FIG. 49A

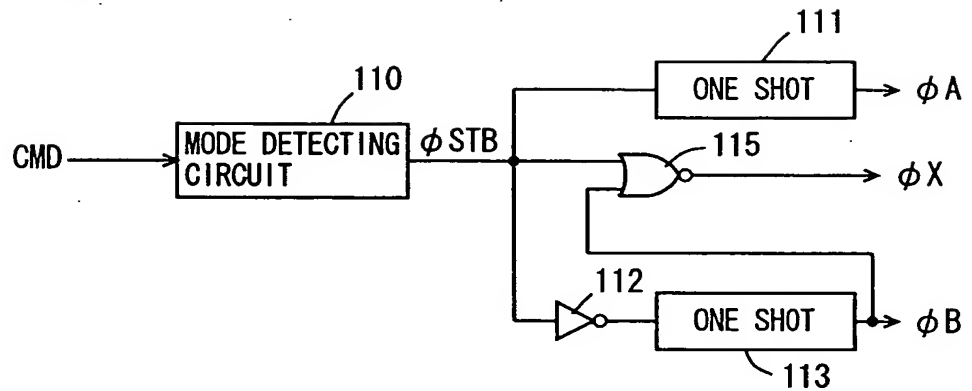


FIG. 49B

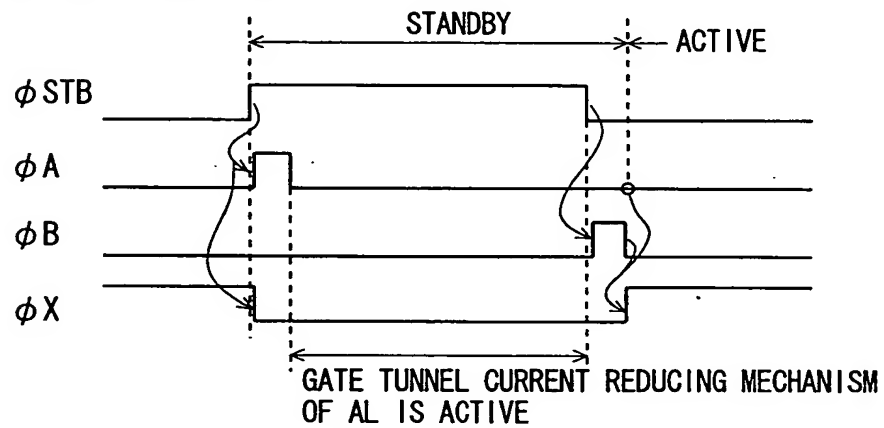


FIG. 50

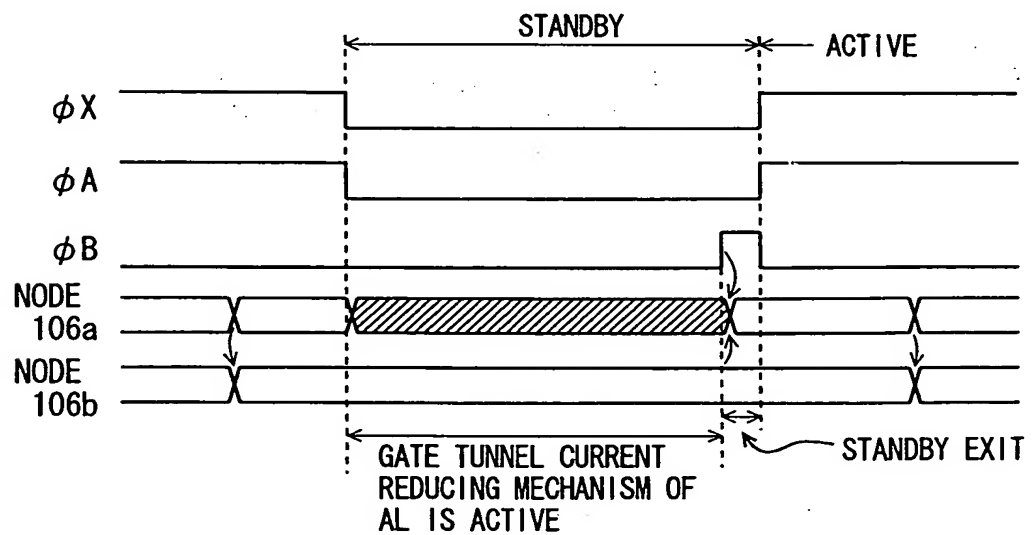


FIG. 51A

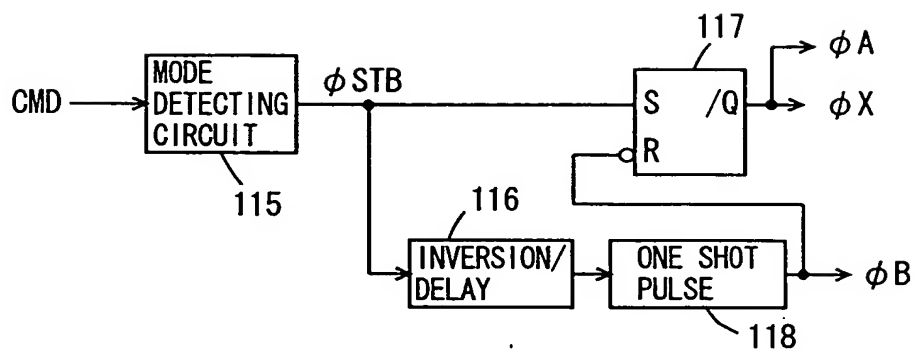


FIG. 51B

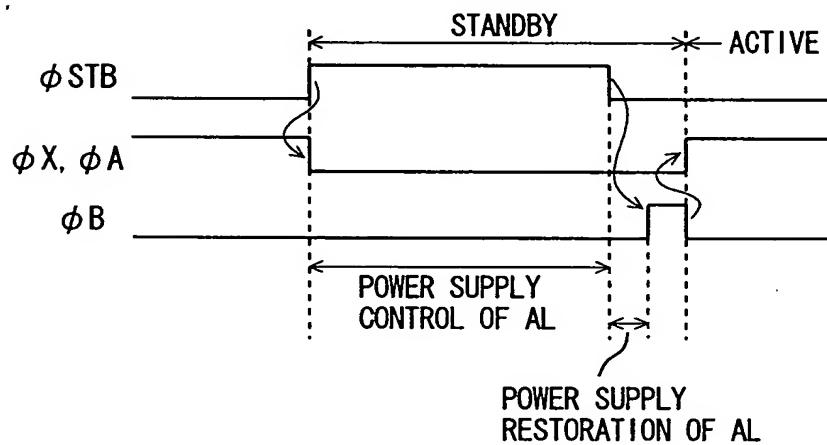


FIG. 52

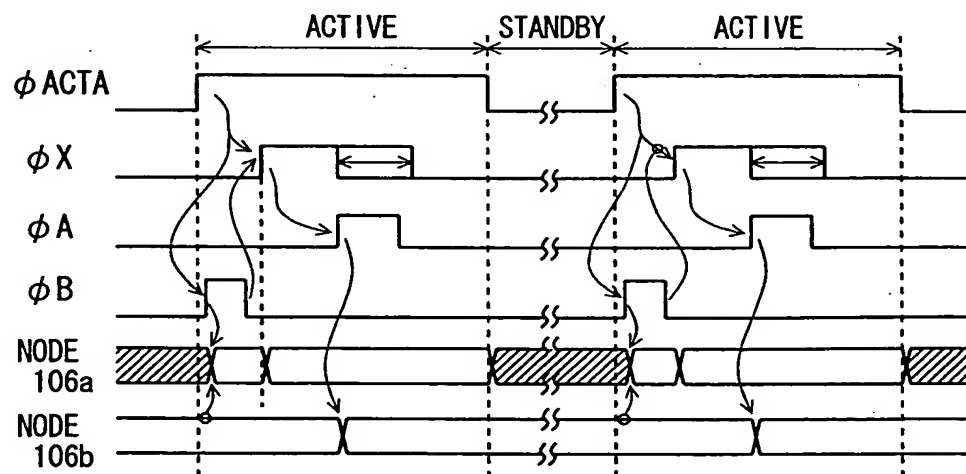


FIG. 53

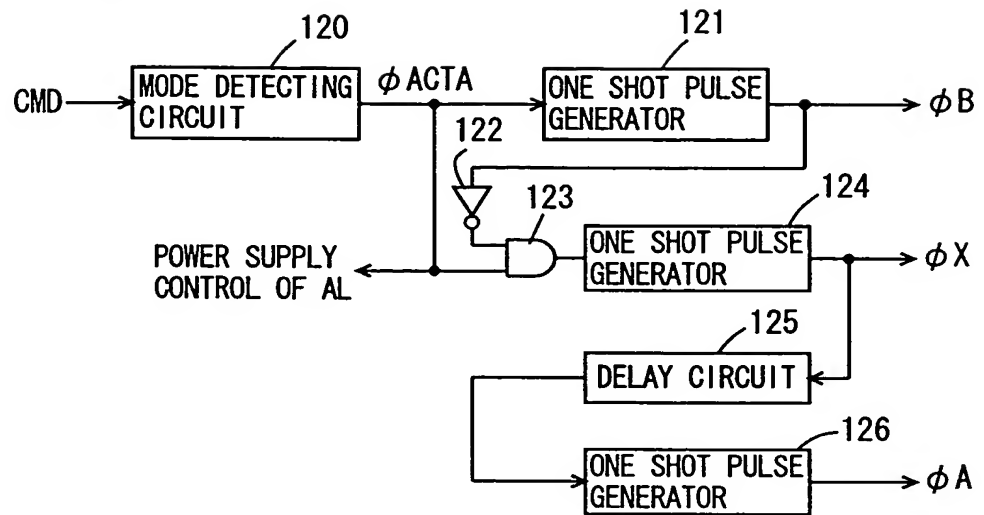


FIG. 54

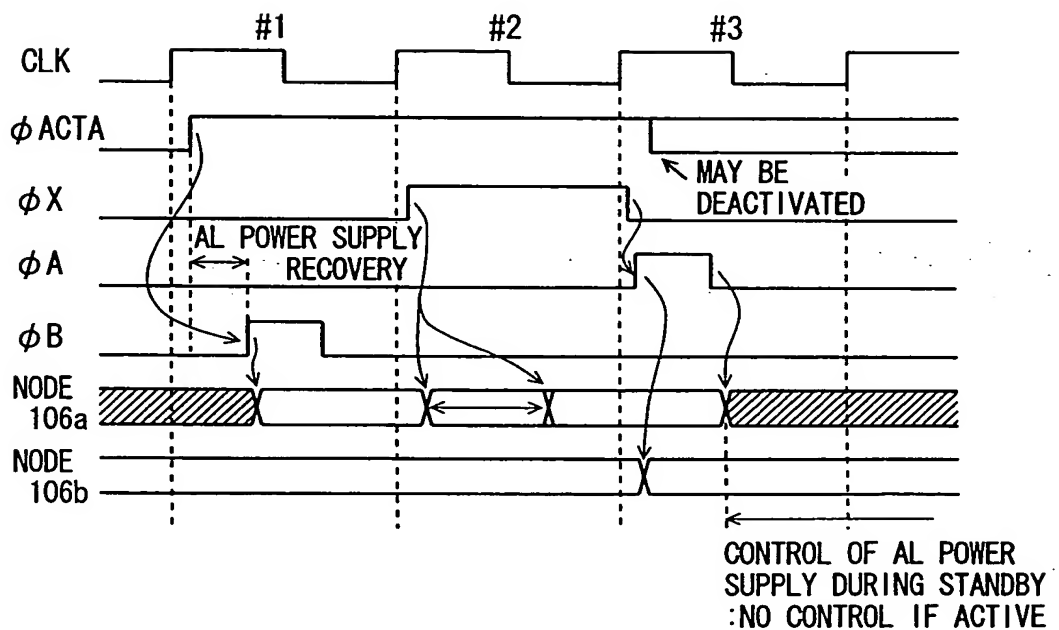


FIG. 55

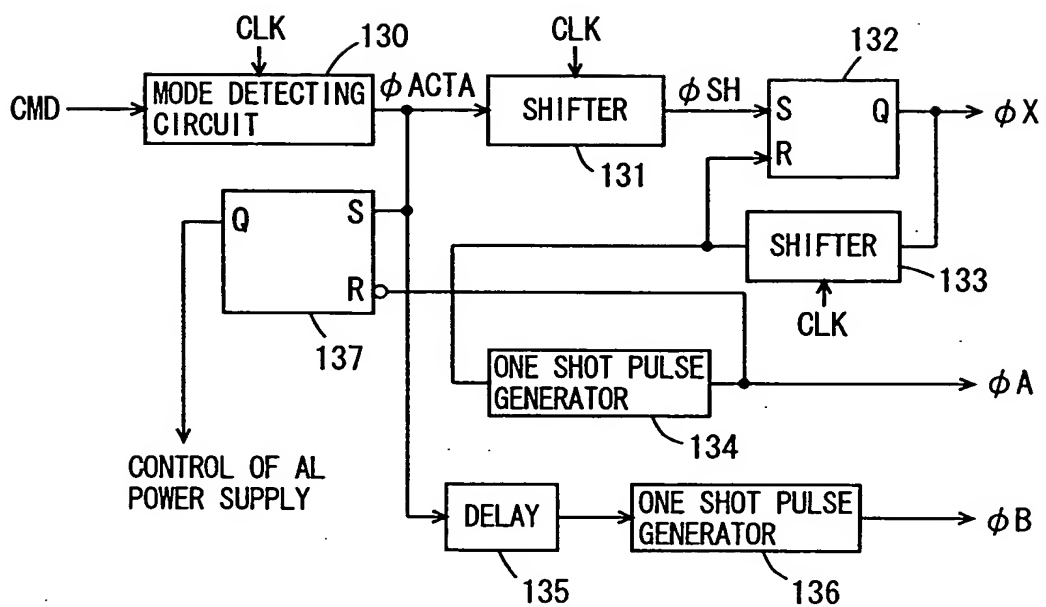


FIG. 56A

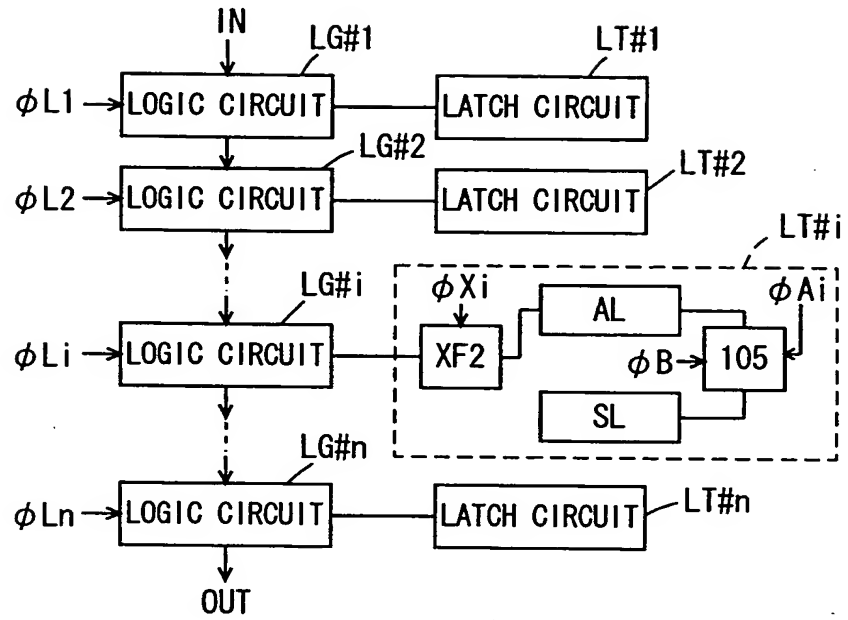


FIG. 56B

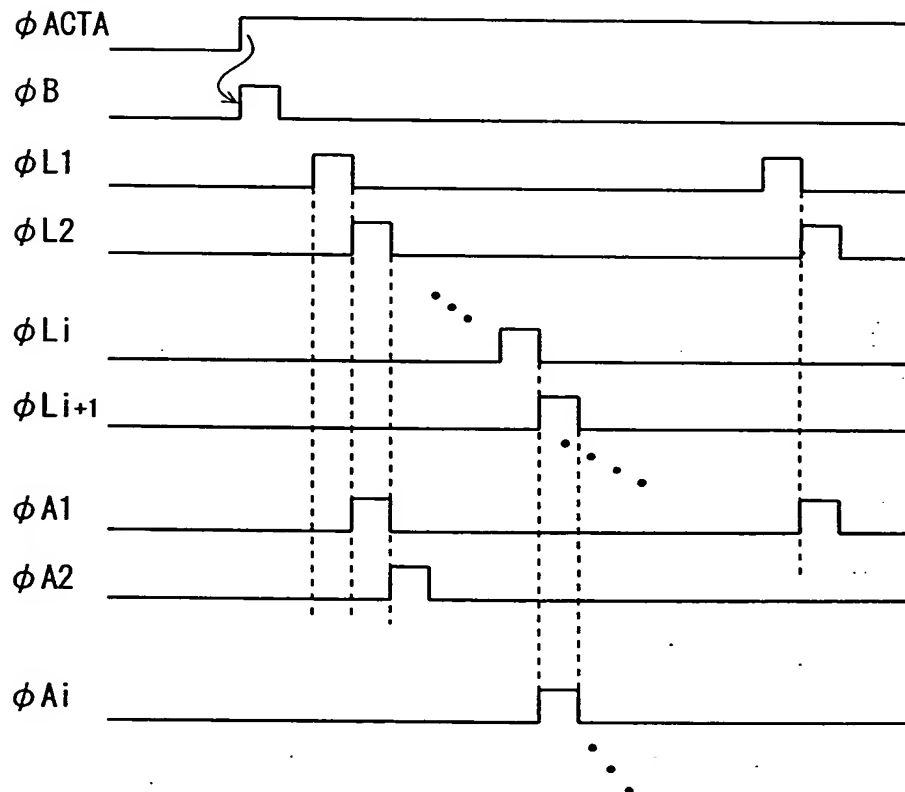


FIG. 57A

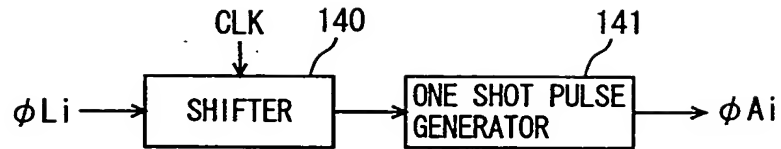


FIG. 57B

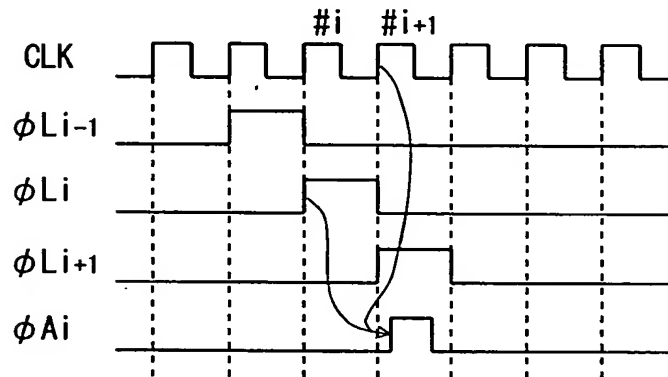


FIG. 58

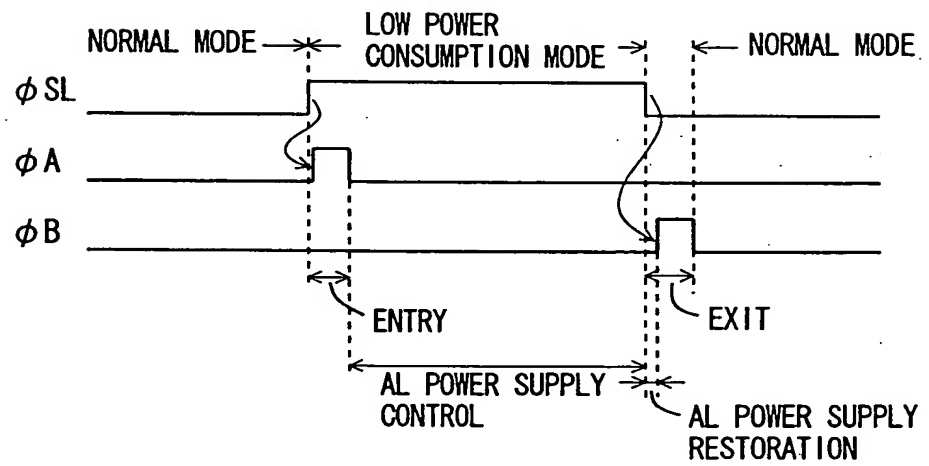


FIG. 59A

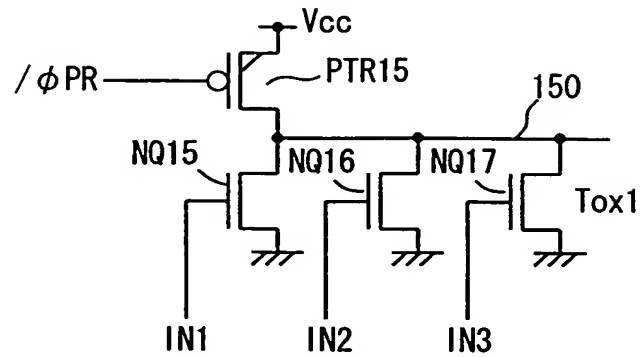


FIG. 59B

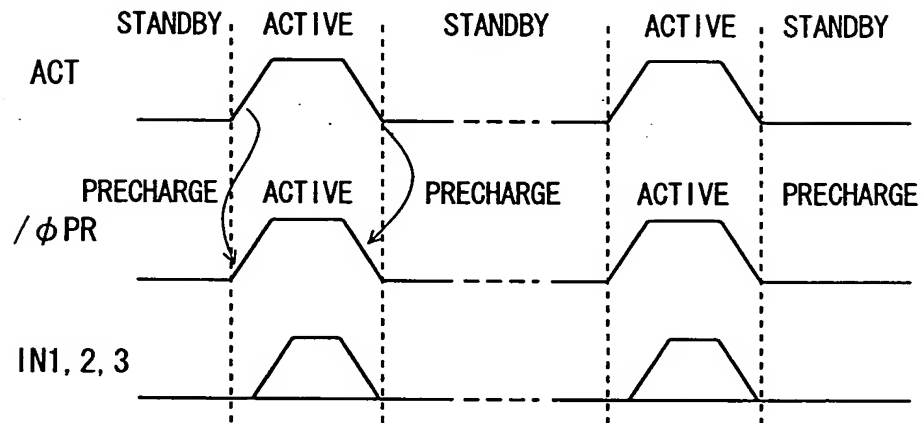


FIG. 59C

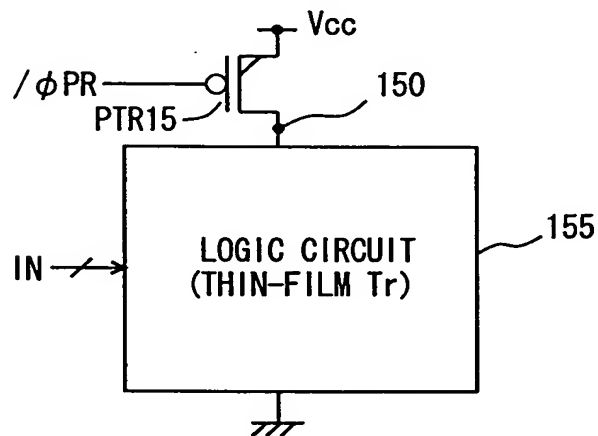


FIG. 60A

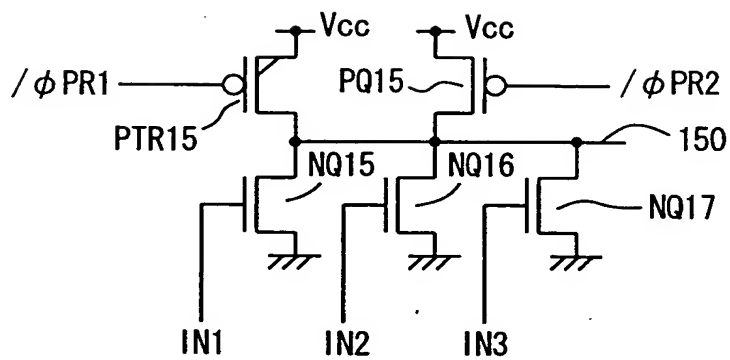


FIG. 60B

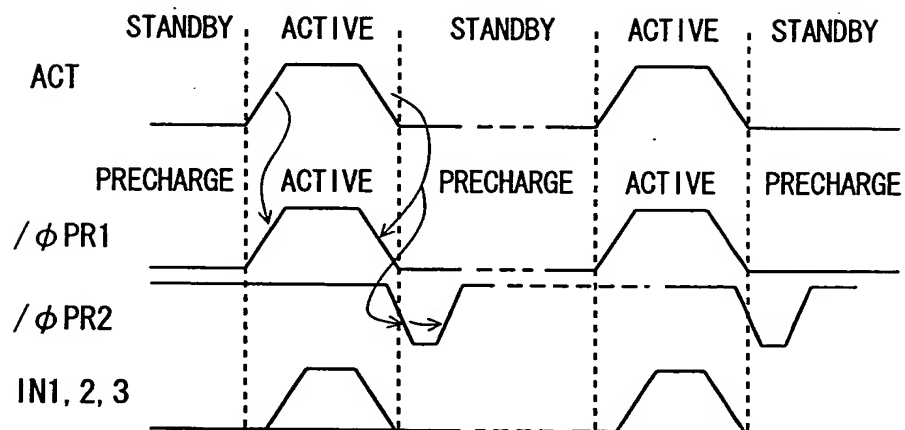


FIG. 61

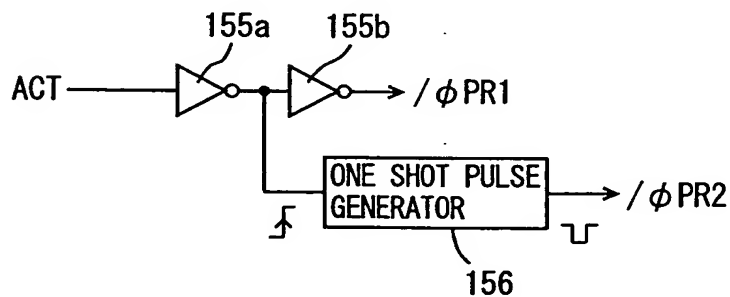


FIG. 62

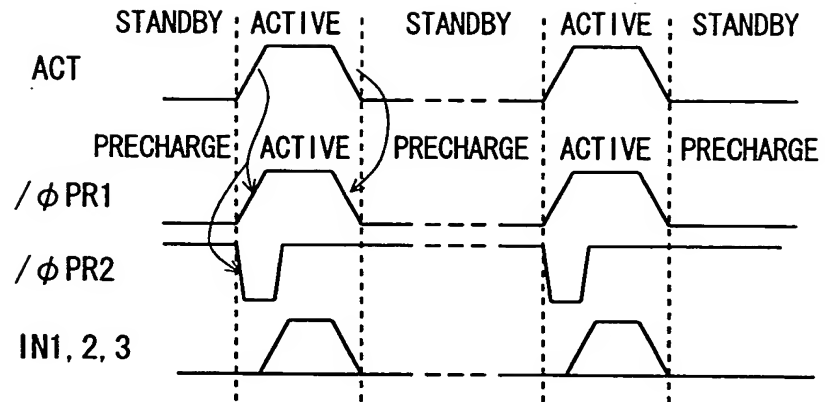


FIG. 63

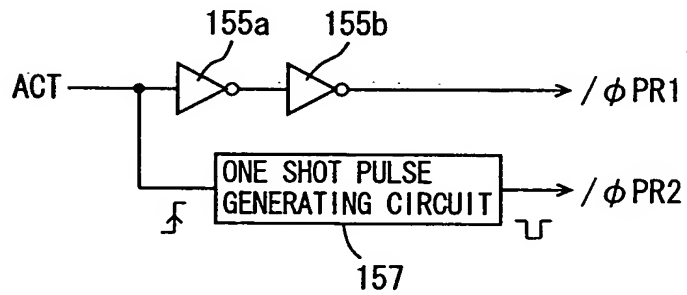


FIG. 64

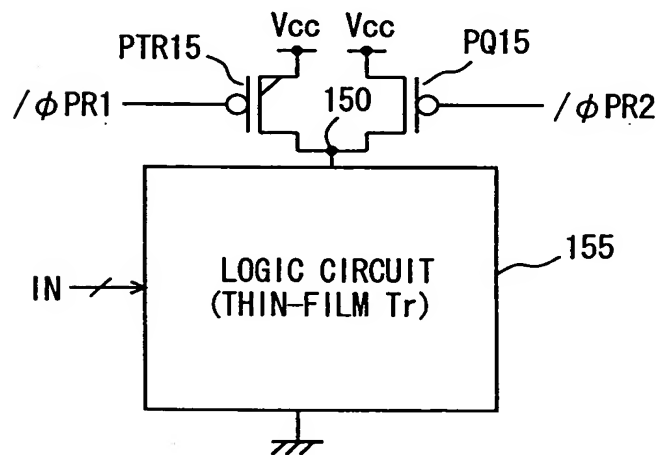


FIG. 65

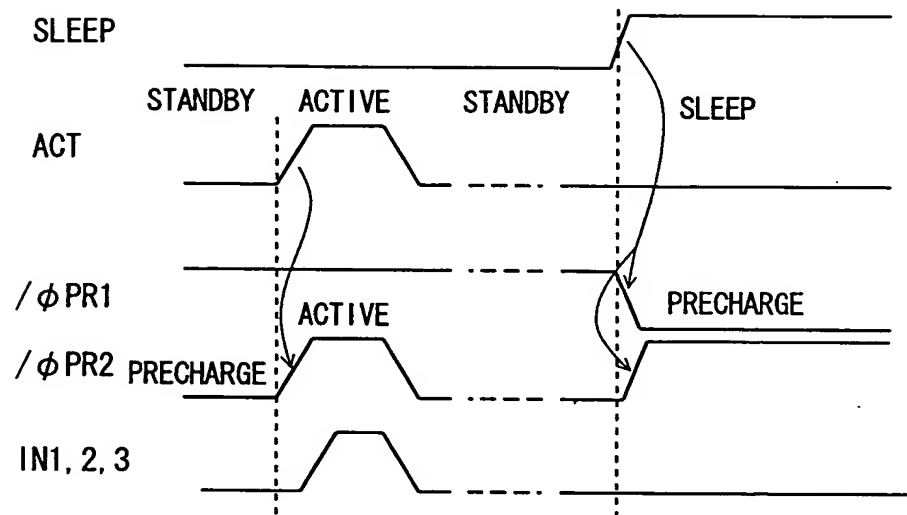


FIG. 66

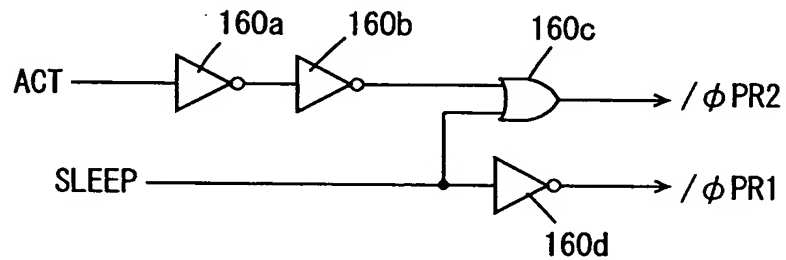


FIG. 67A

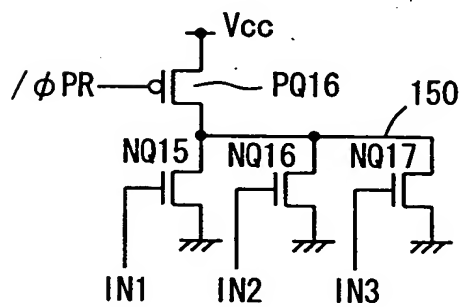


FIG. 67B

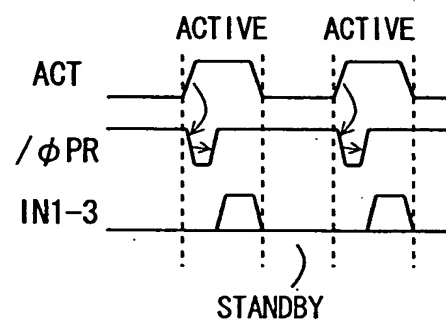


FIG. 68

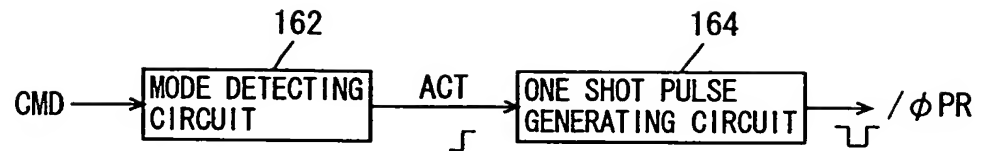


FIG. 69

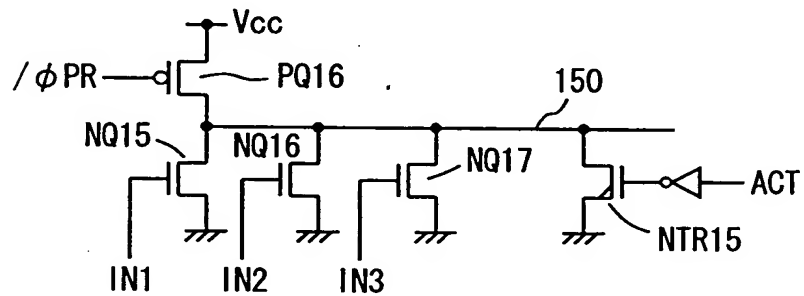


FIG. 70

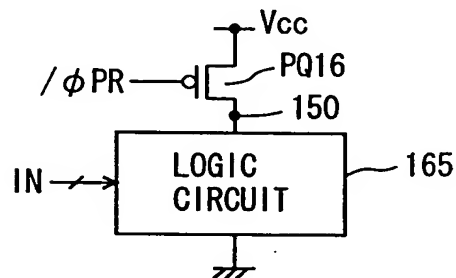


FIG. 71

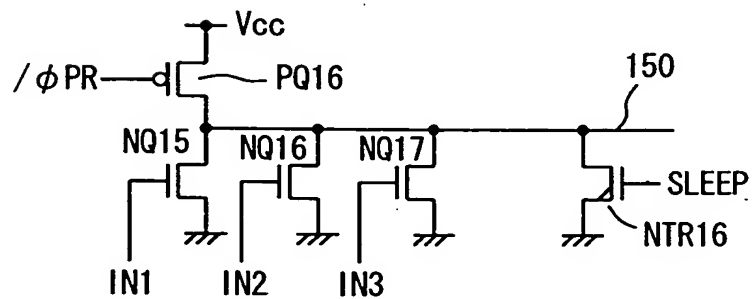


FIG. 72

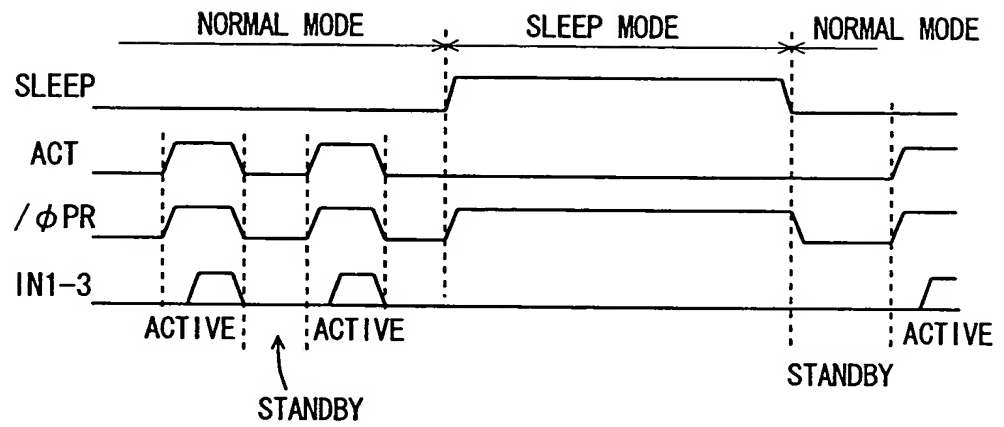


FIG. 73

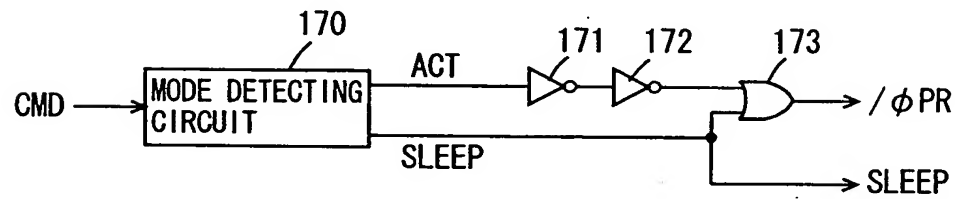


FIG. 74A

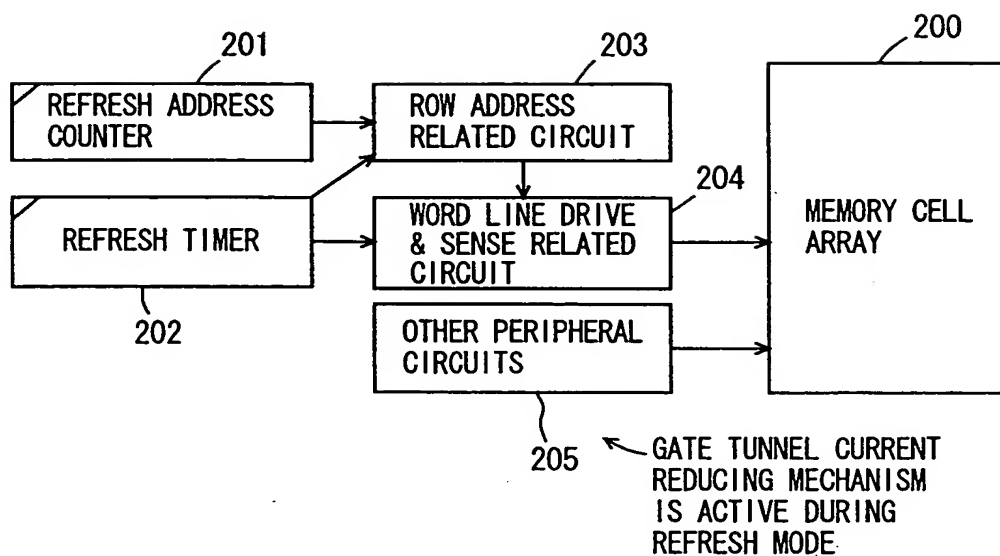


FIG. 74B

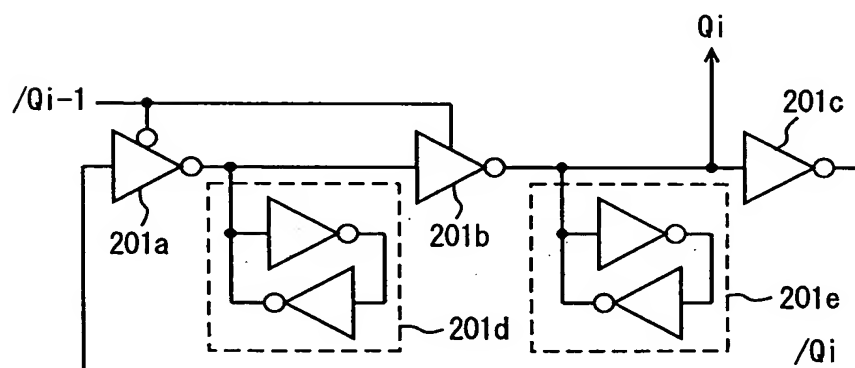


FIG. 75

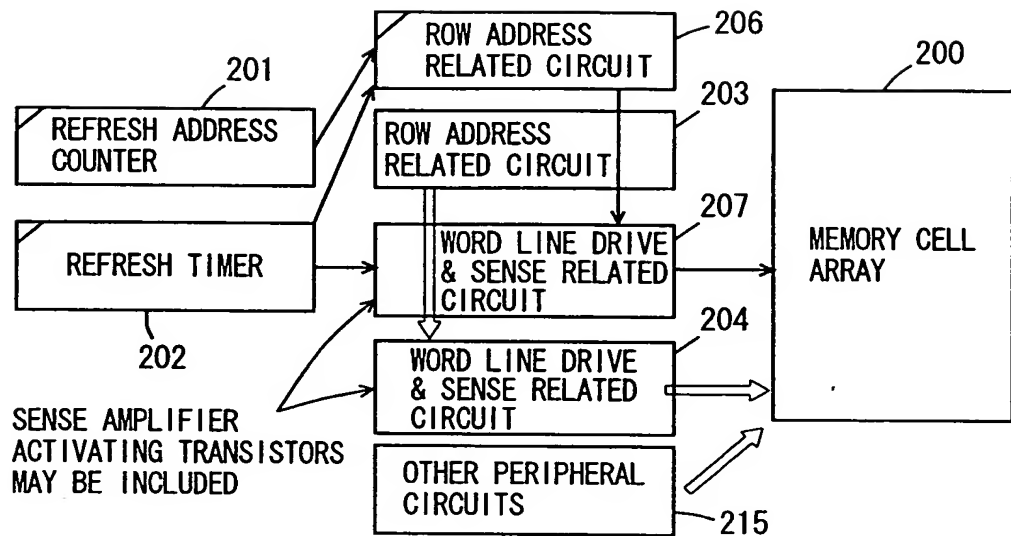


FIG. 76

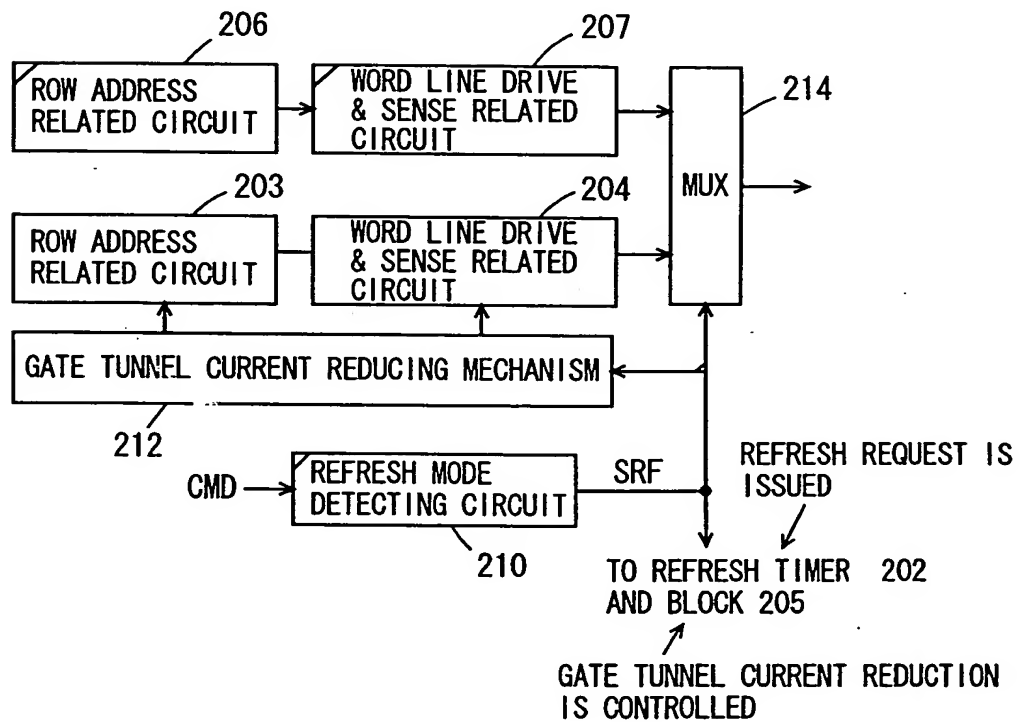


FIG. 77

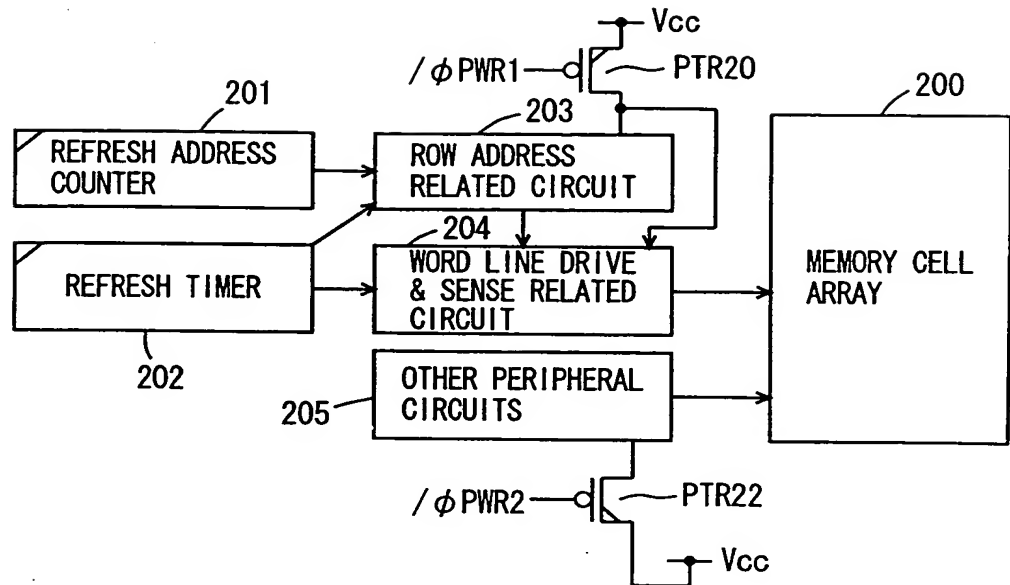


FIG. 78

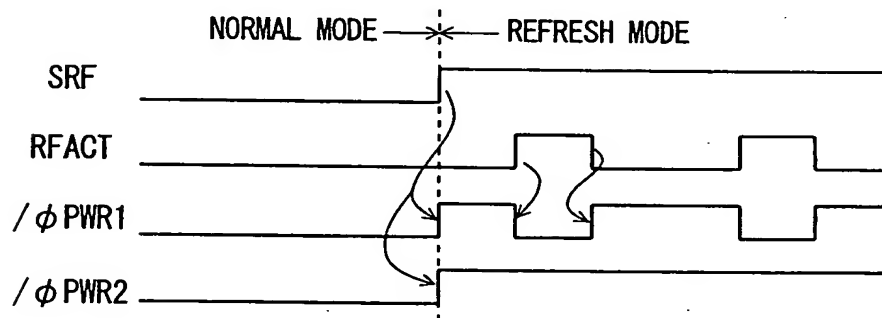


FIG. 79

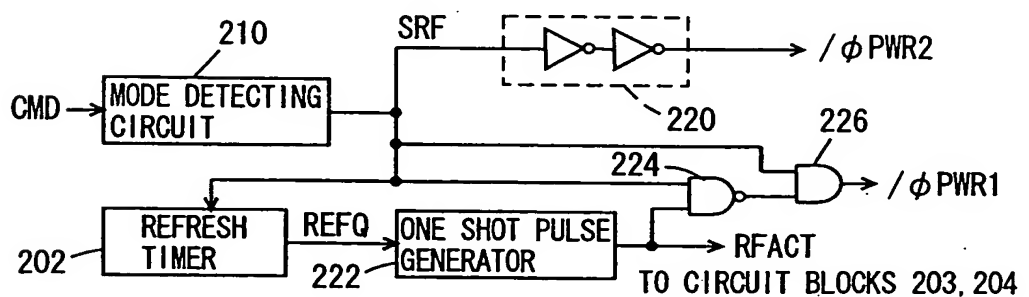


FIG. 80

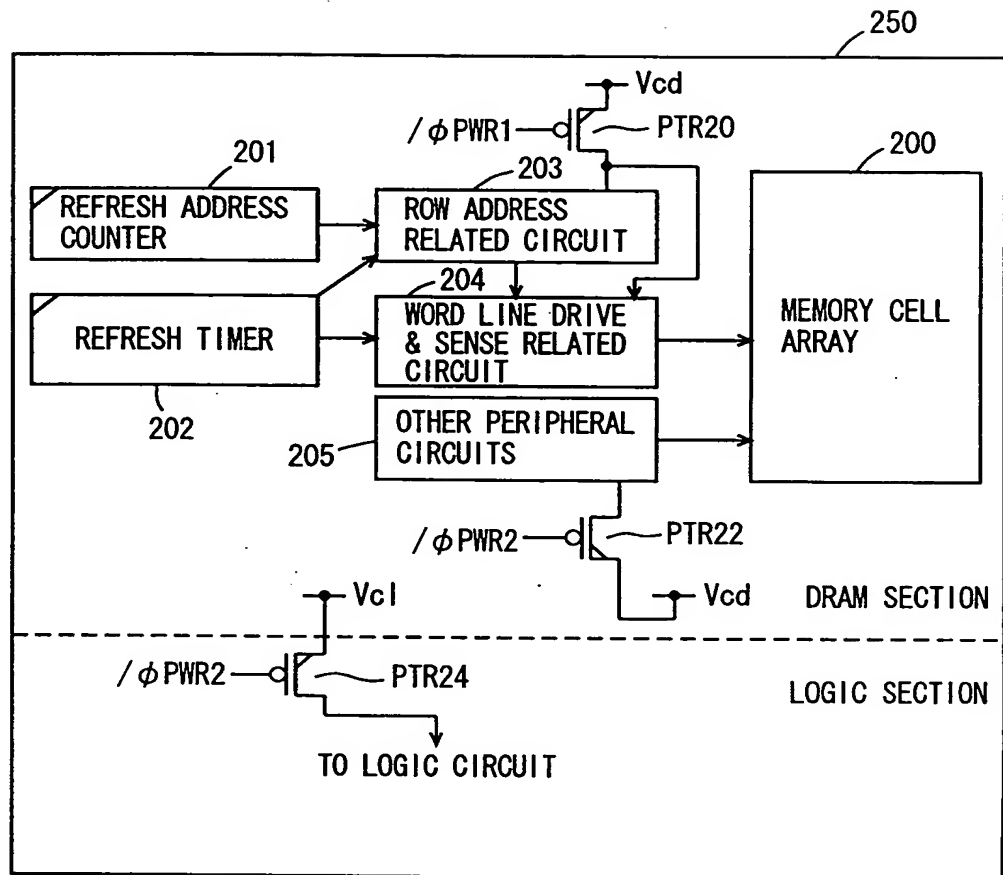


FIG. 81

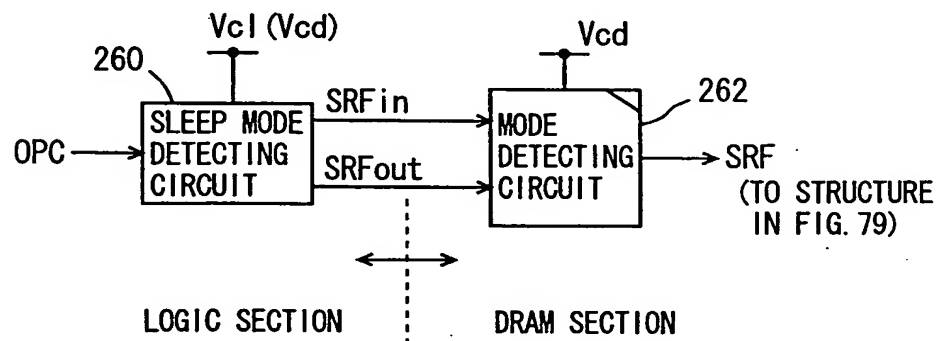


FIG. 82

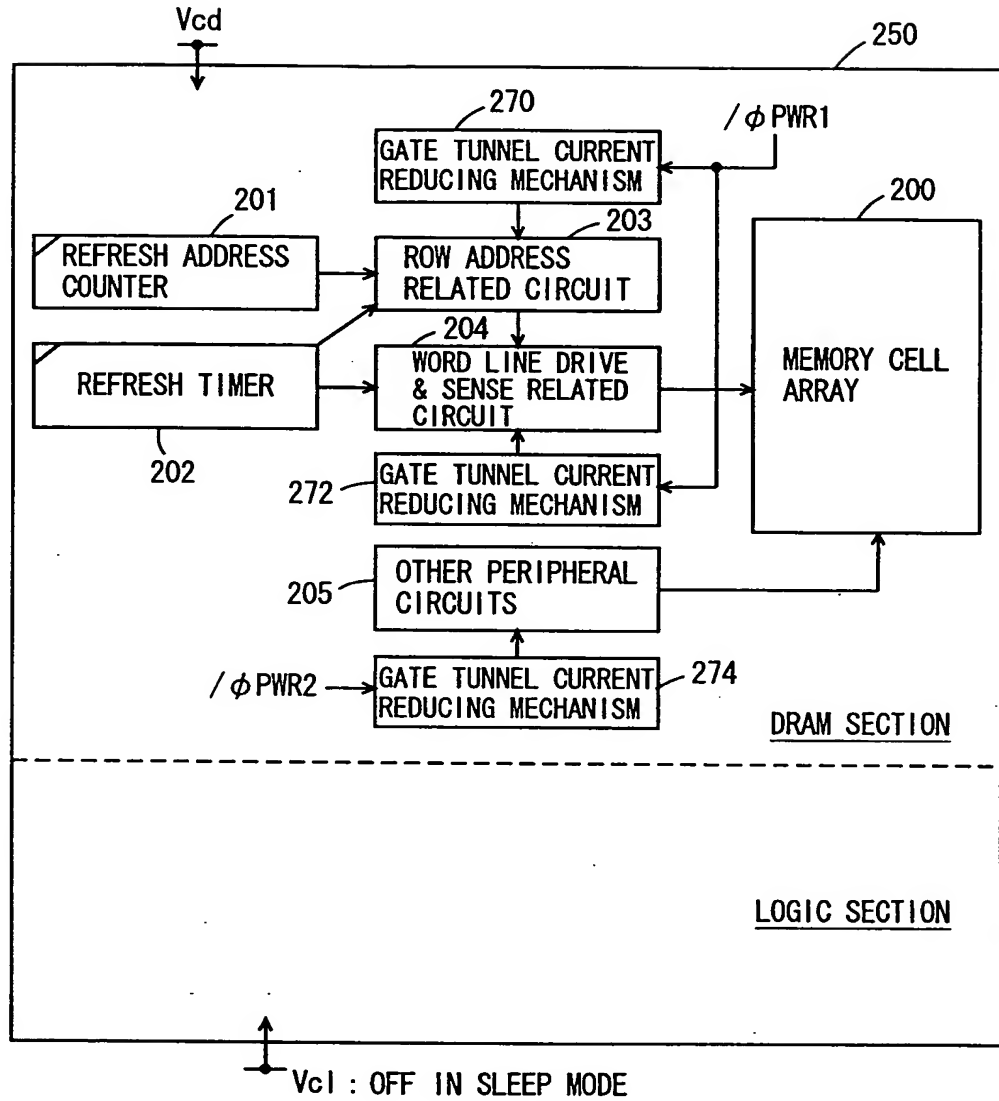


FIG. 83

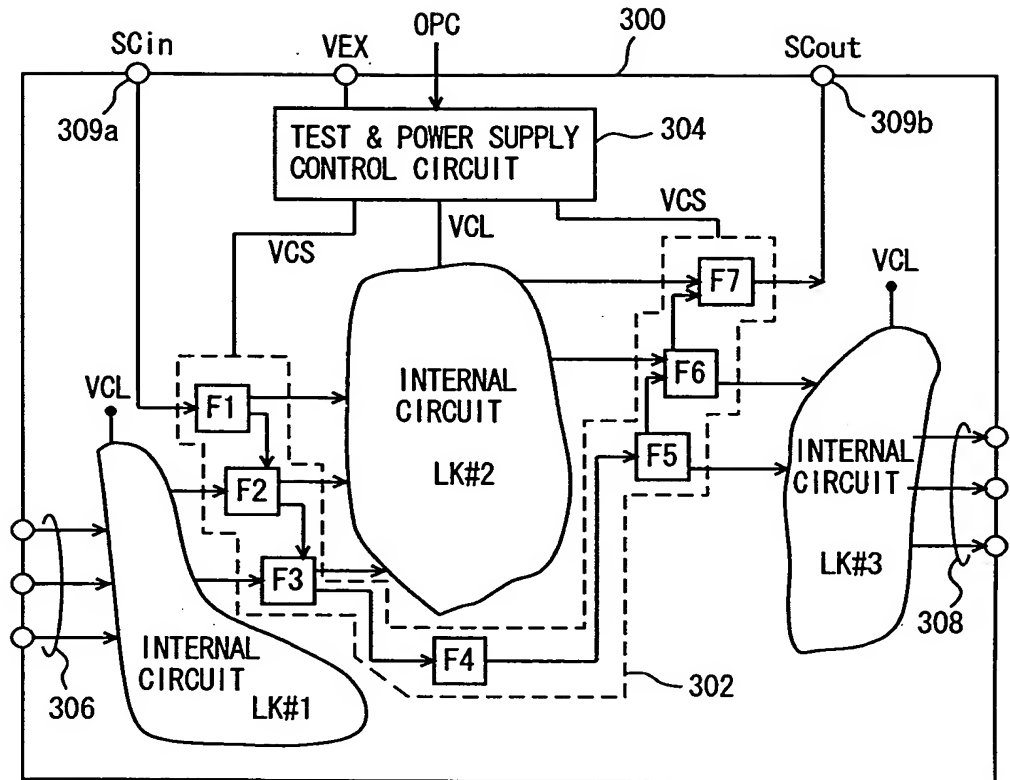


FIG. 84

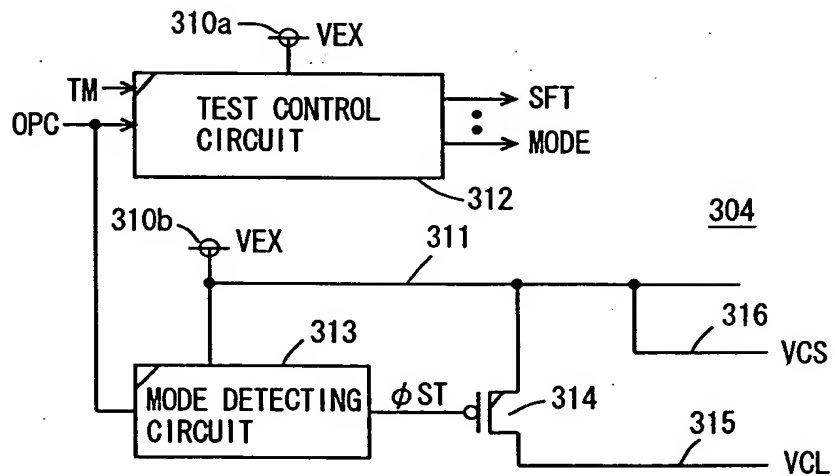


FIG. 85

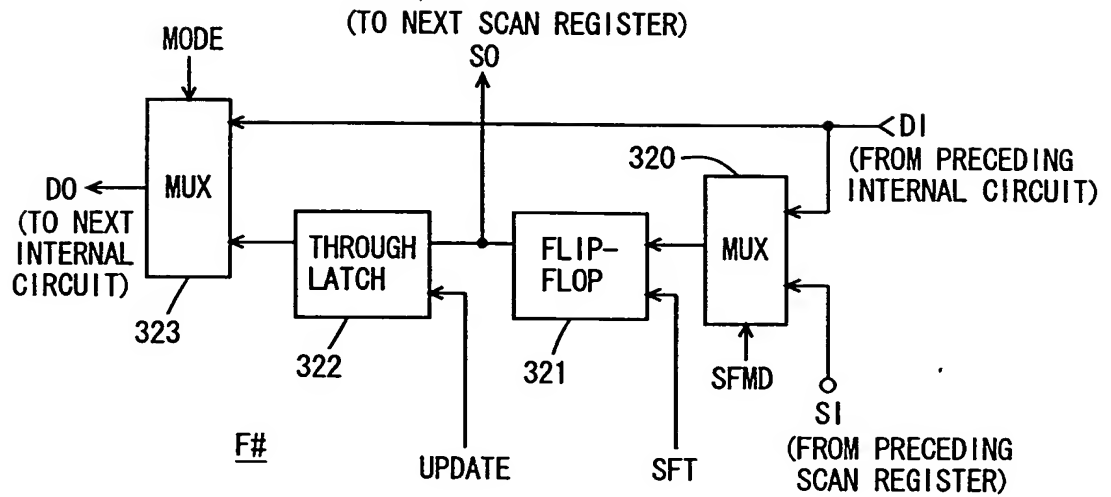


FIG. 86

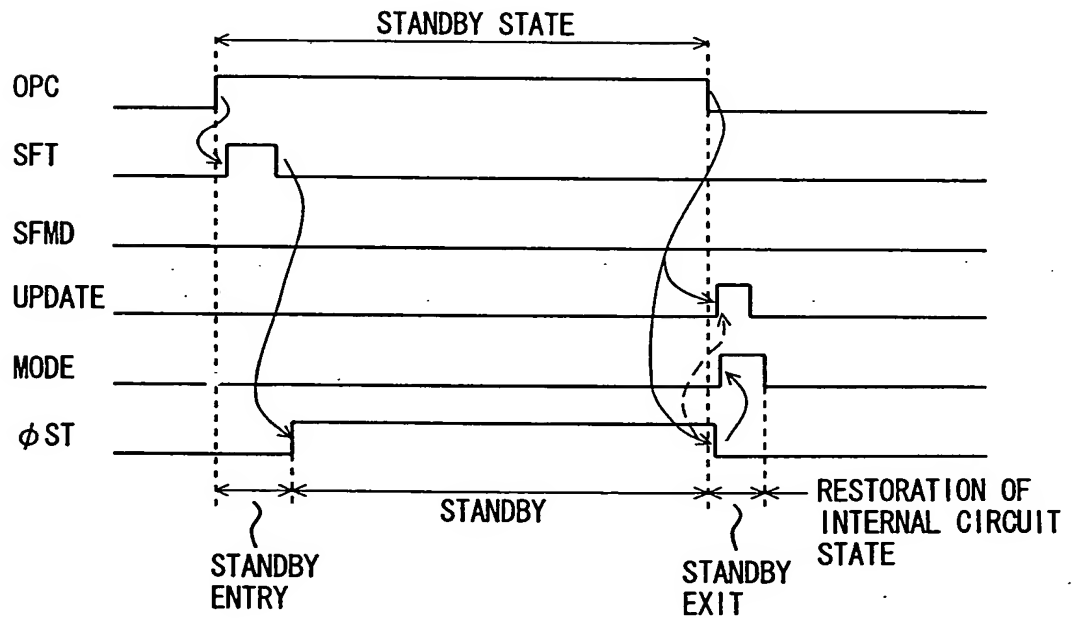


FIG. 87

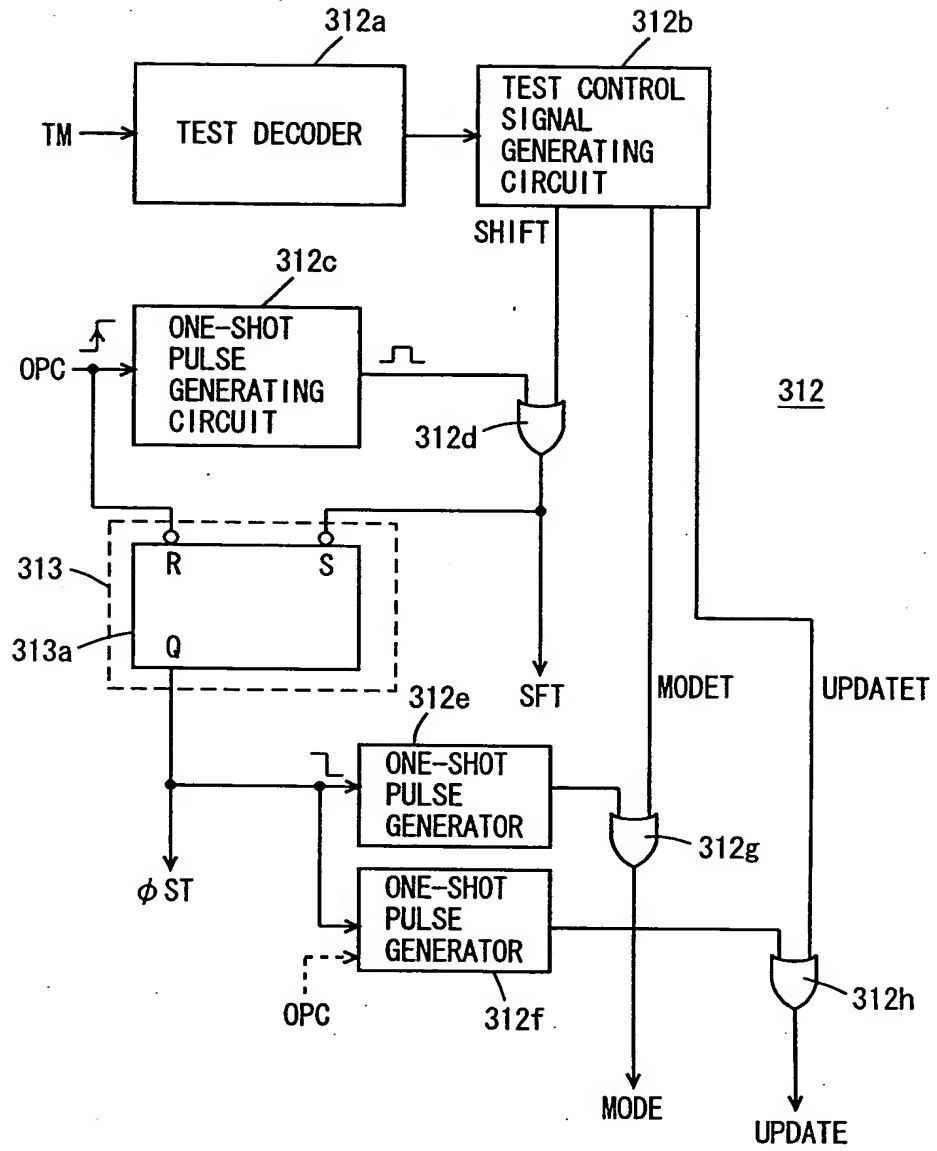


FIG. 88

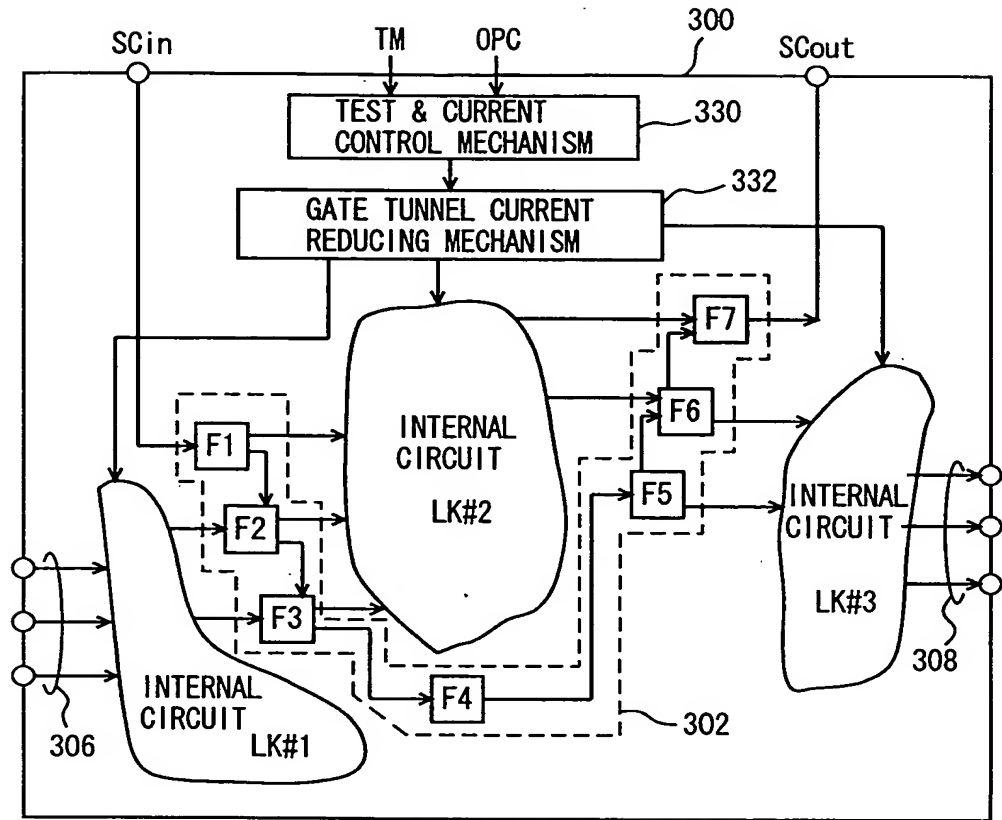


FIG. 89

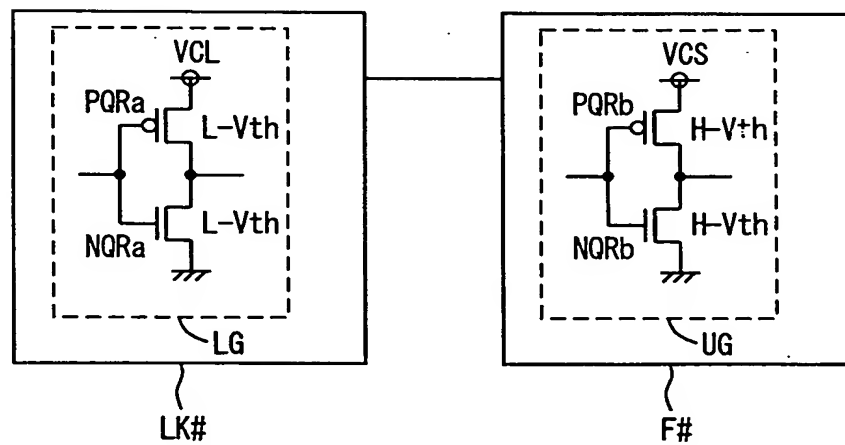


FIG. 90

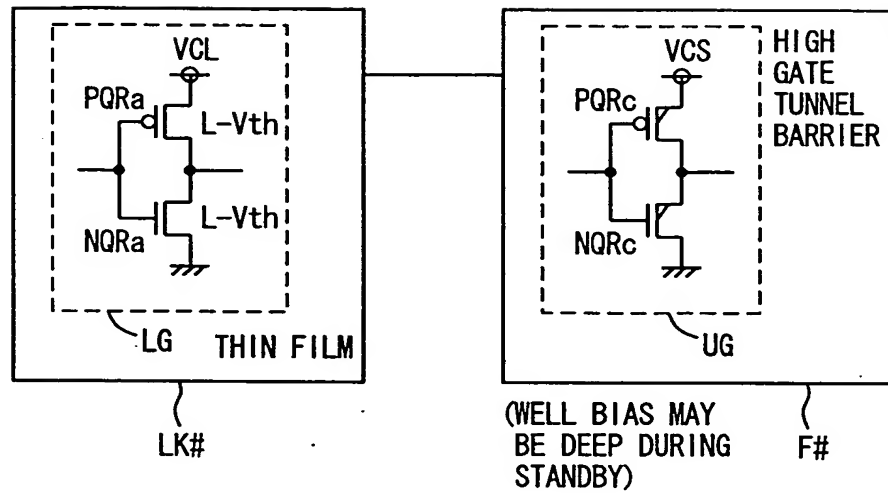


FIG. 91

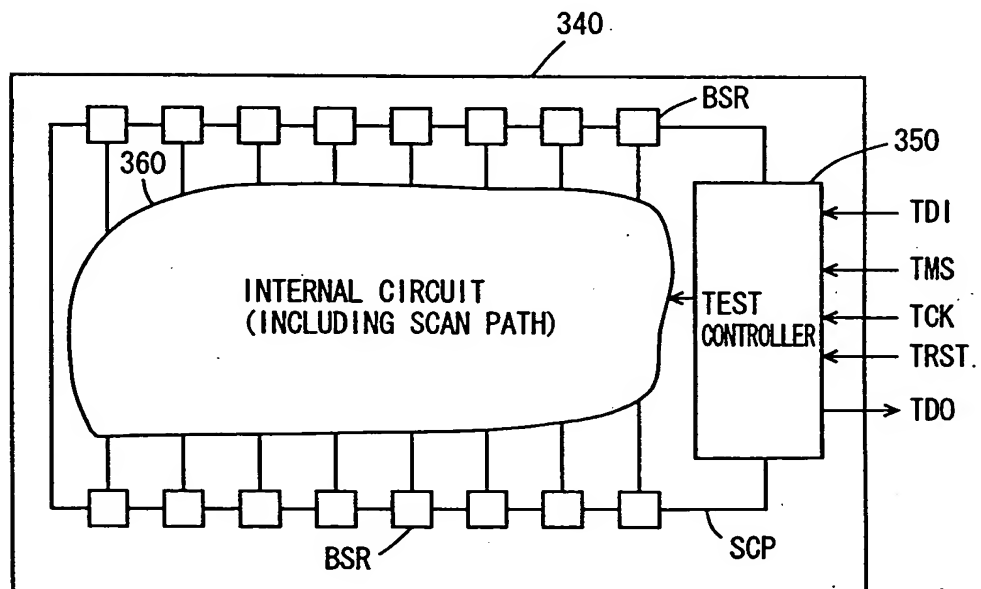


FIG. 92

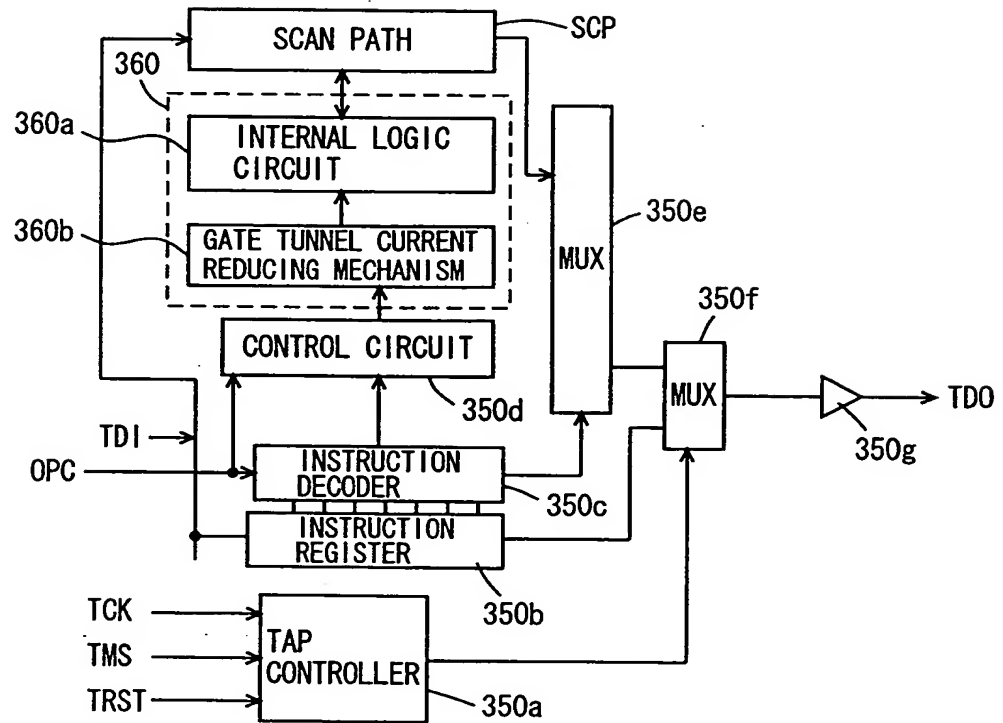


FIG. 93

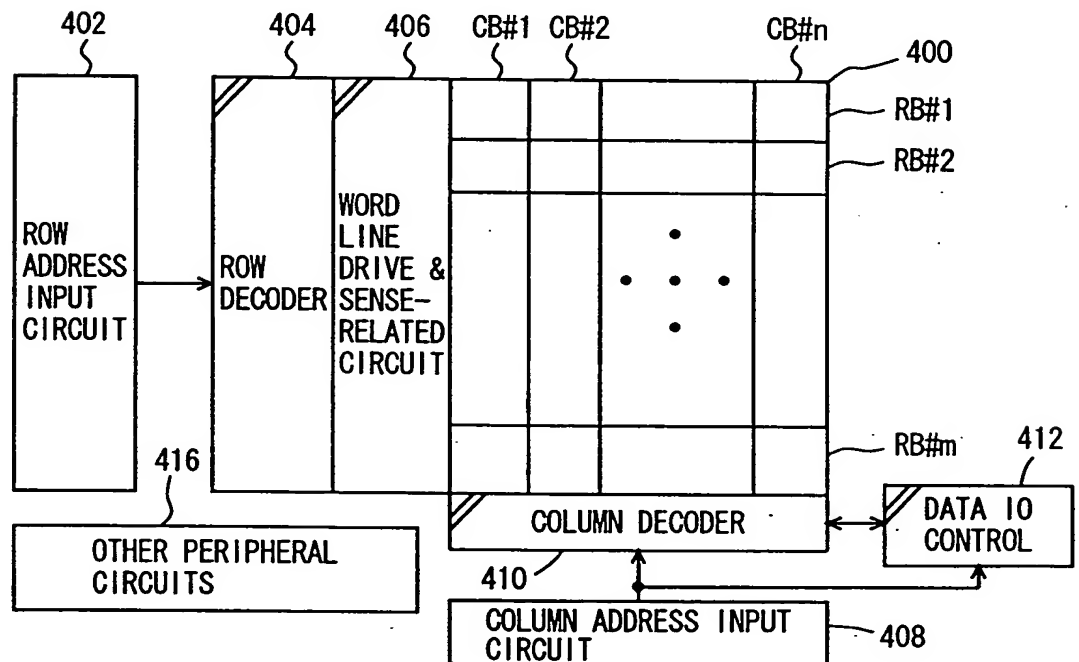


FIG. 96

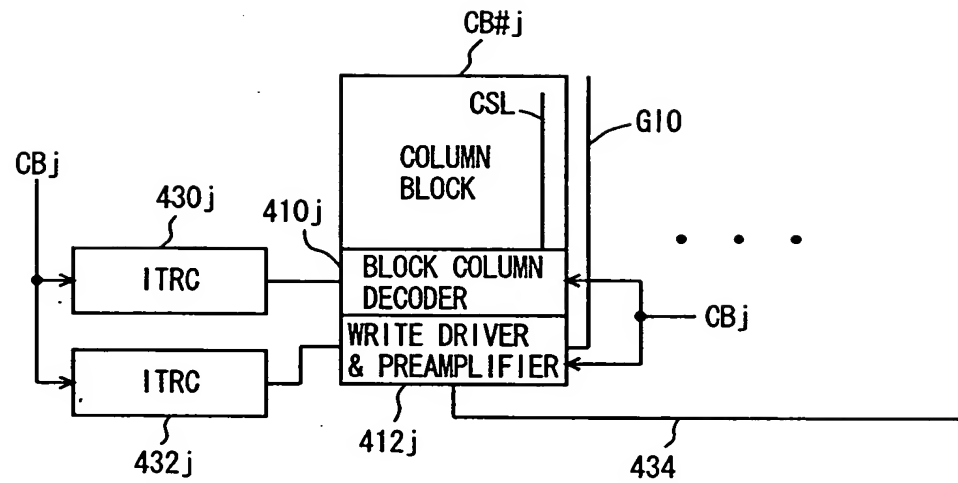


FIG. 97

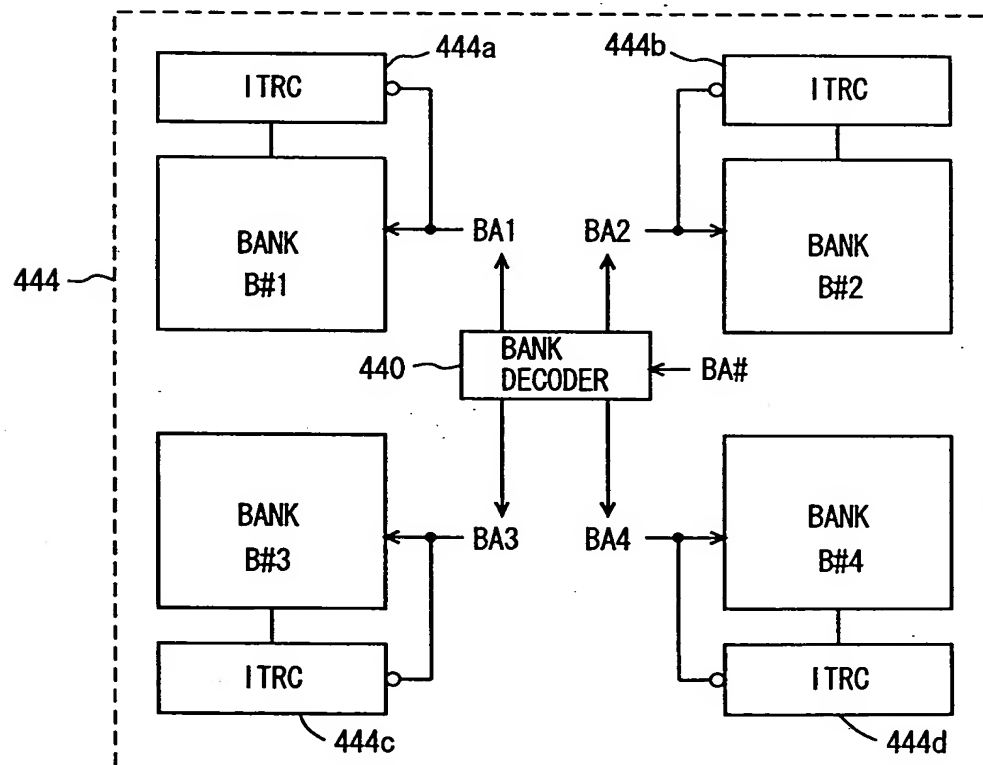


FIG. 98

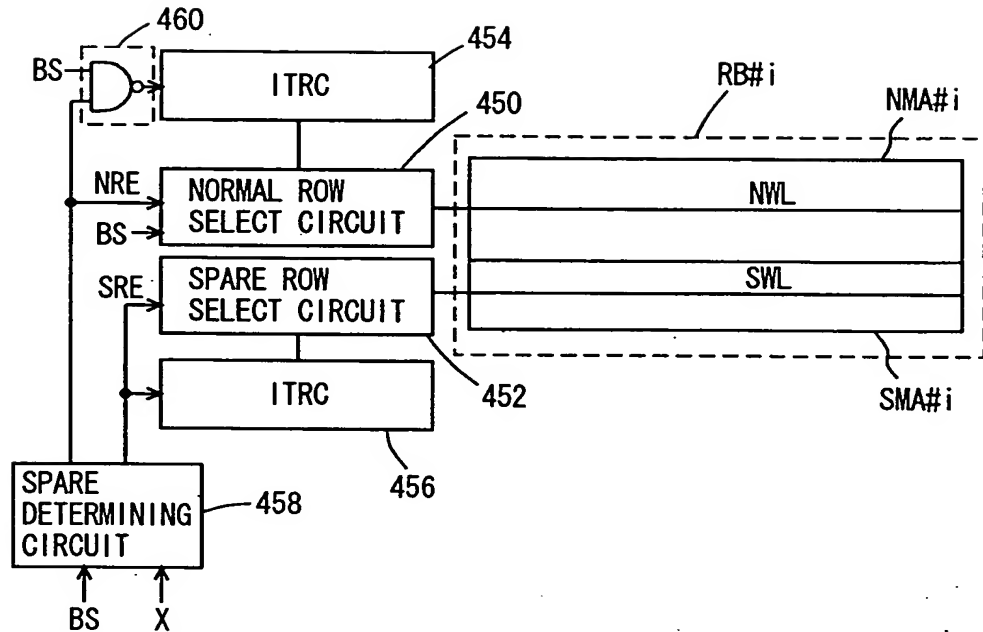


FIG. 99

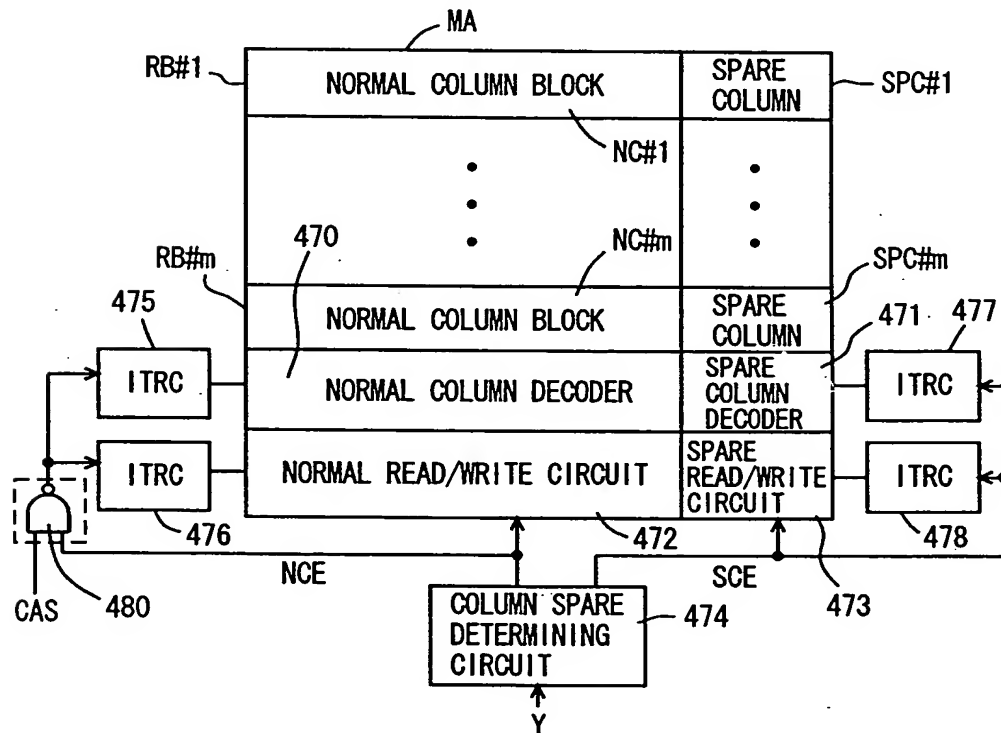


FIG. 100

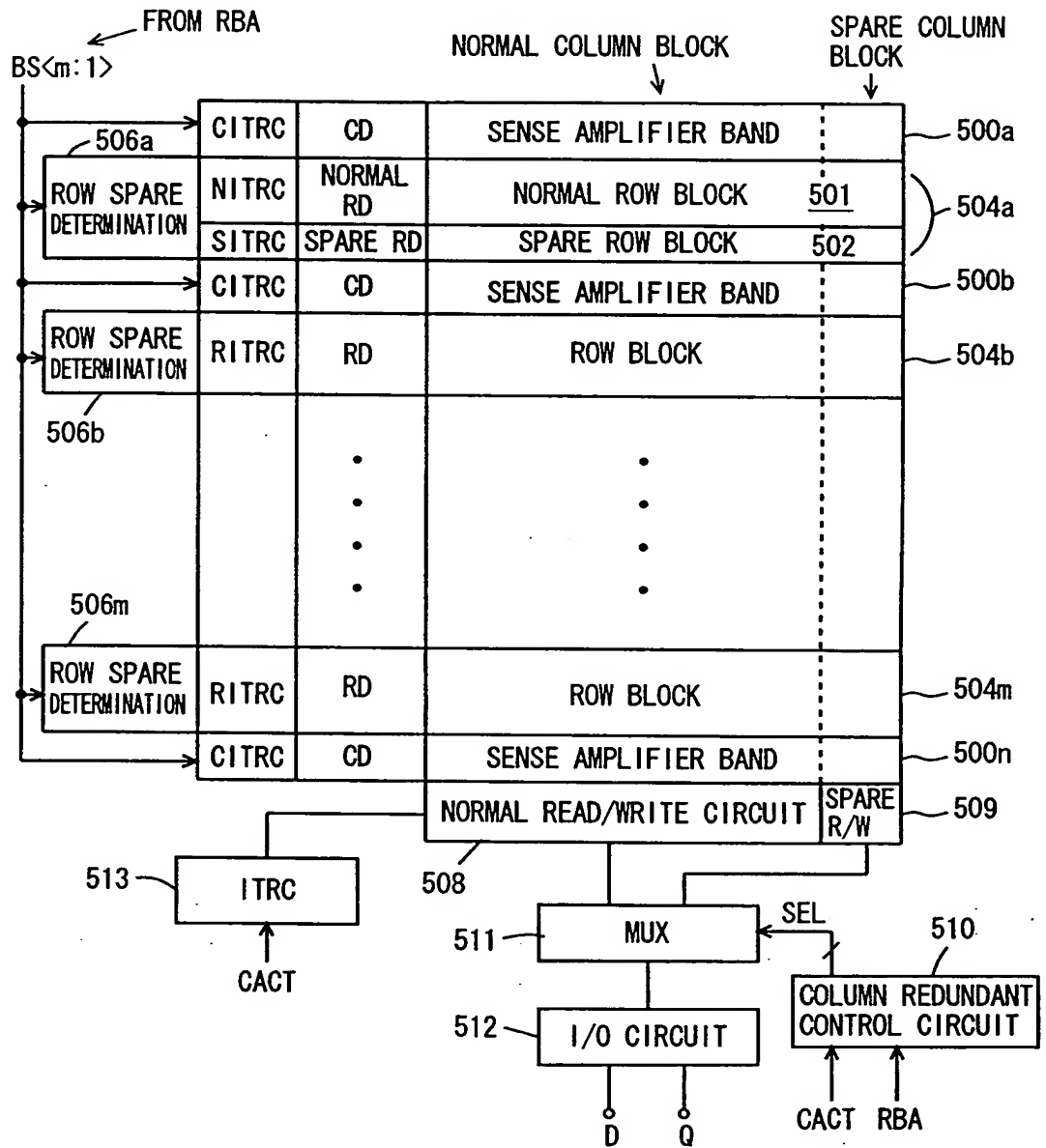


FIG. 101A

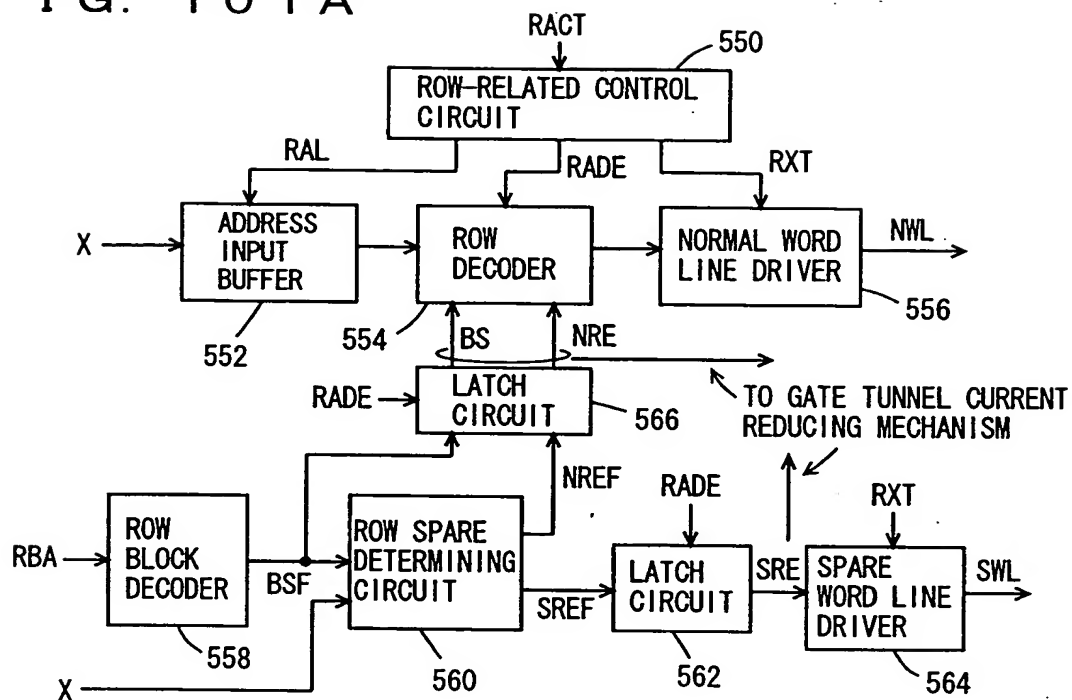


FIG. 101B

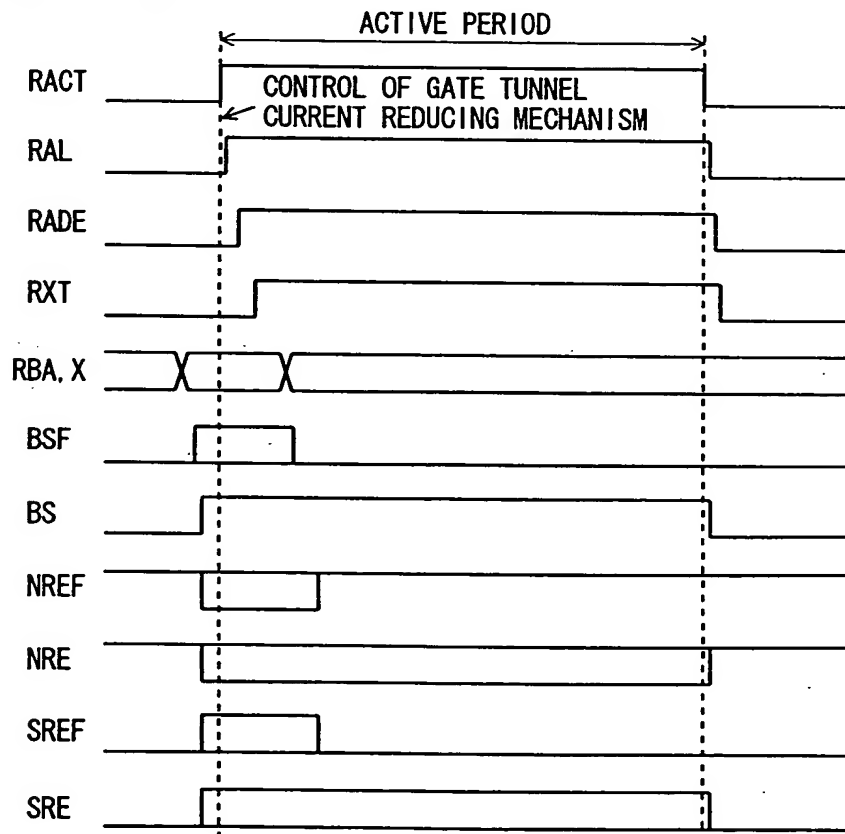


FIG. 102

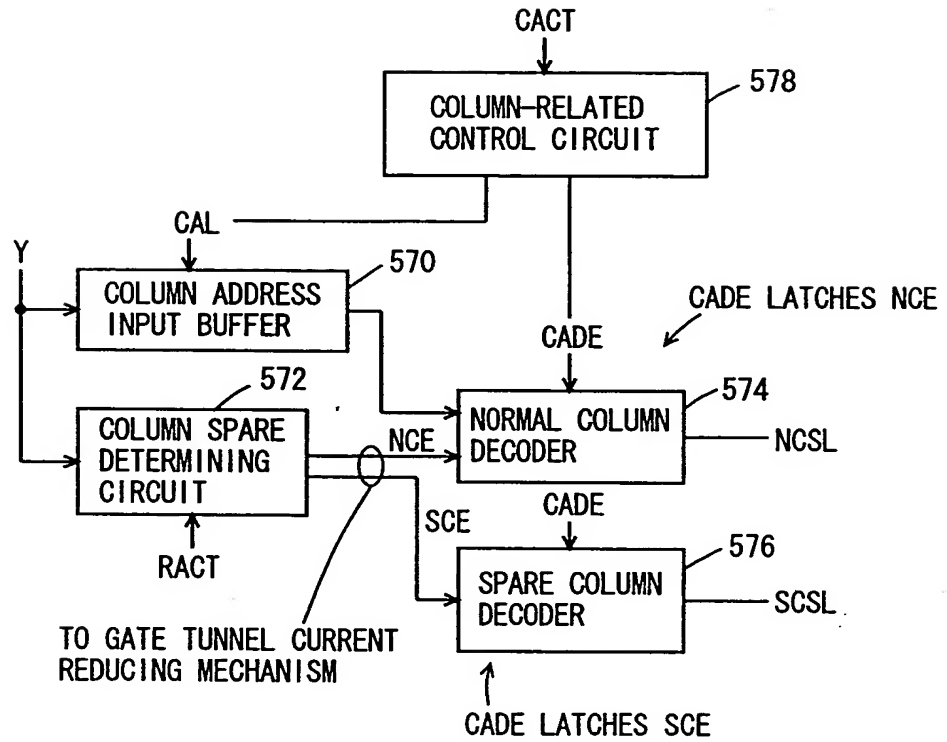


FIG. 103

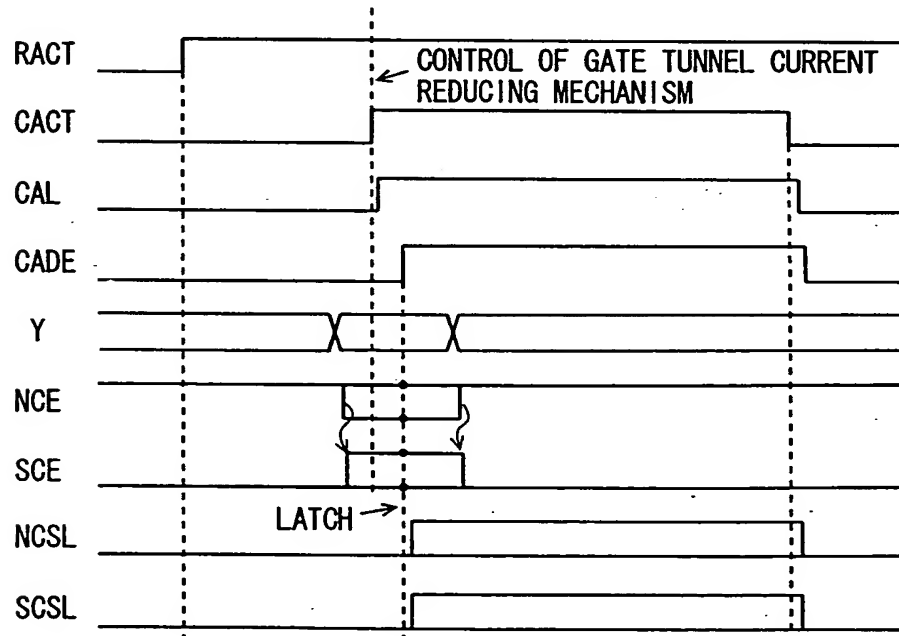


FIG. 104 PRIOR ART

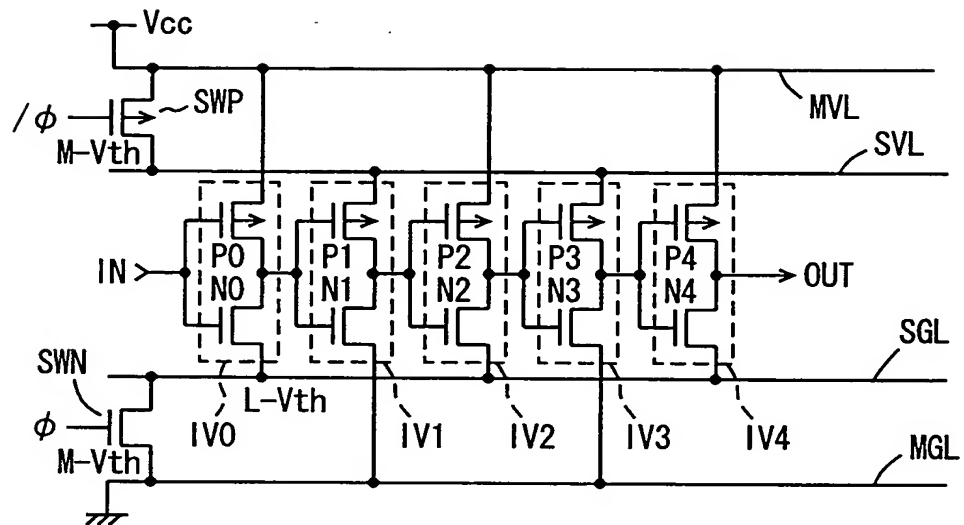


FIG. 105 PRIOR ART

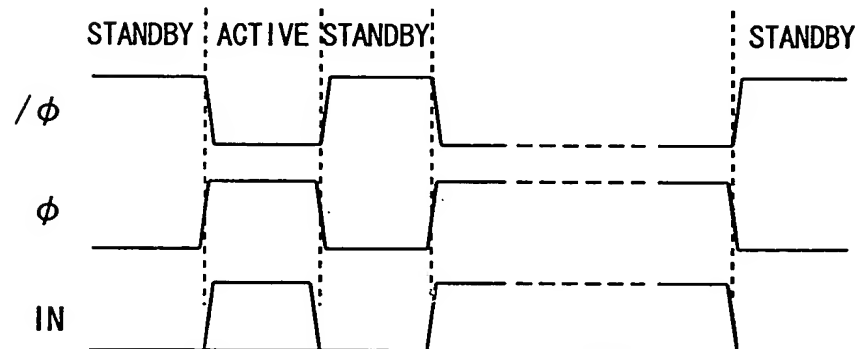


FIG. 106A PRIOR ART

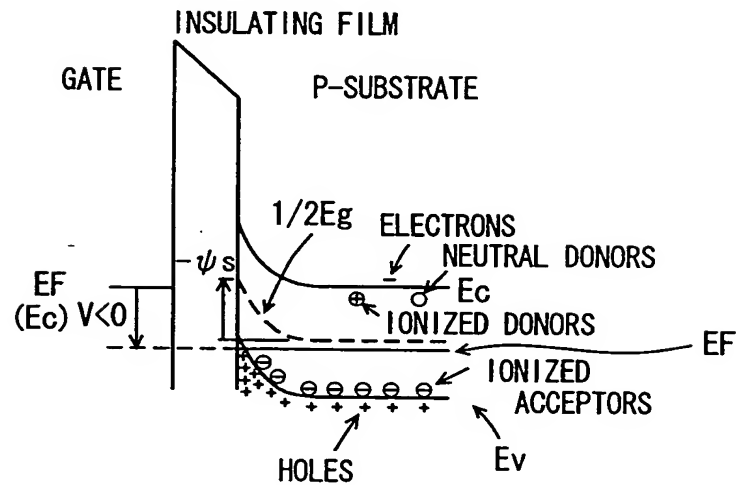


FIG. 106B PRIOR ART

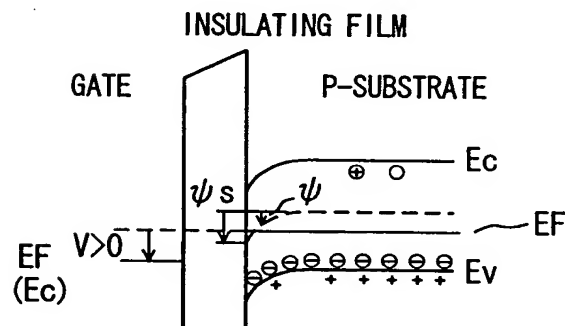


FIG. 106C PRIOR ART

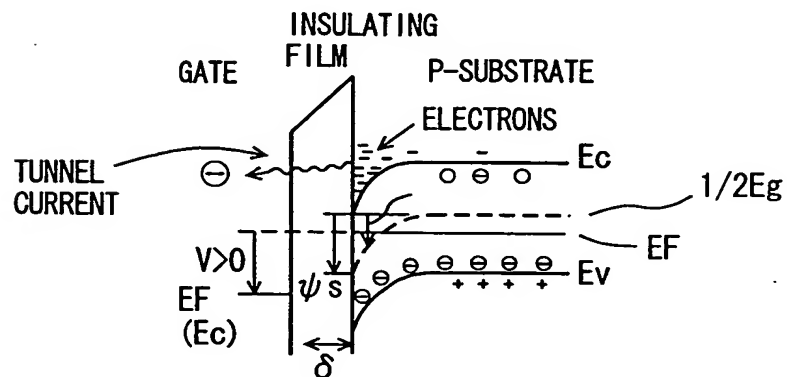


FIG. 107 PRIOR ART

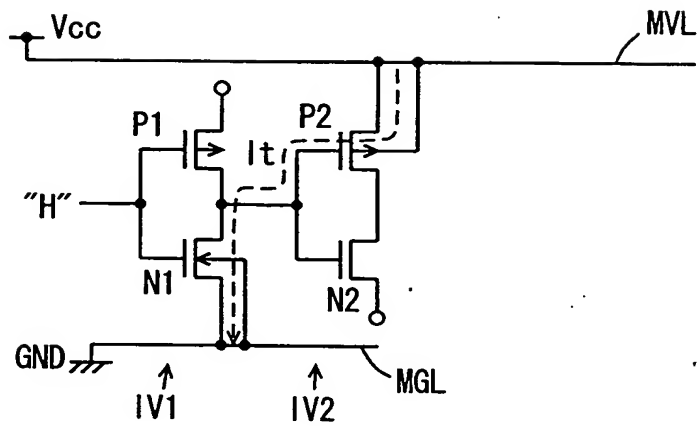


FIG. 108 PRIOR ART

